

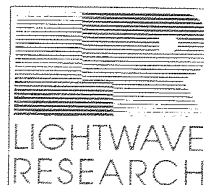
intellabeam[®] 700 HX

Fixture



TM

High End Systems, Inc.
2217 West Braker Lane
Austin, Texas, U.S.A.



TM

*Schematic
Drawings*

intellabeam 700 HX Fixture

Schematic Drawings

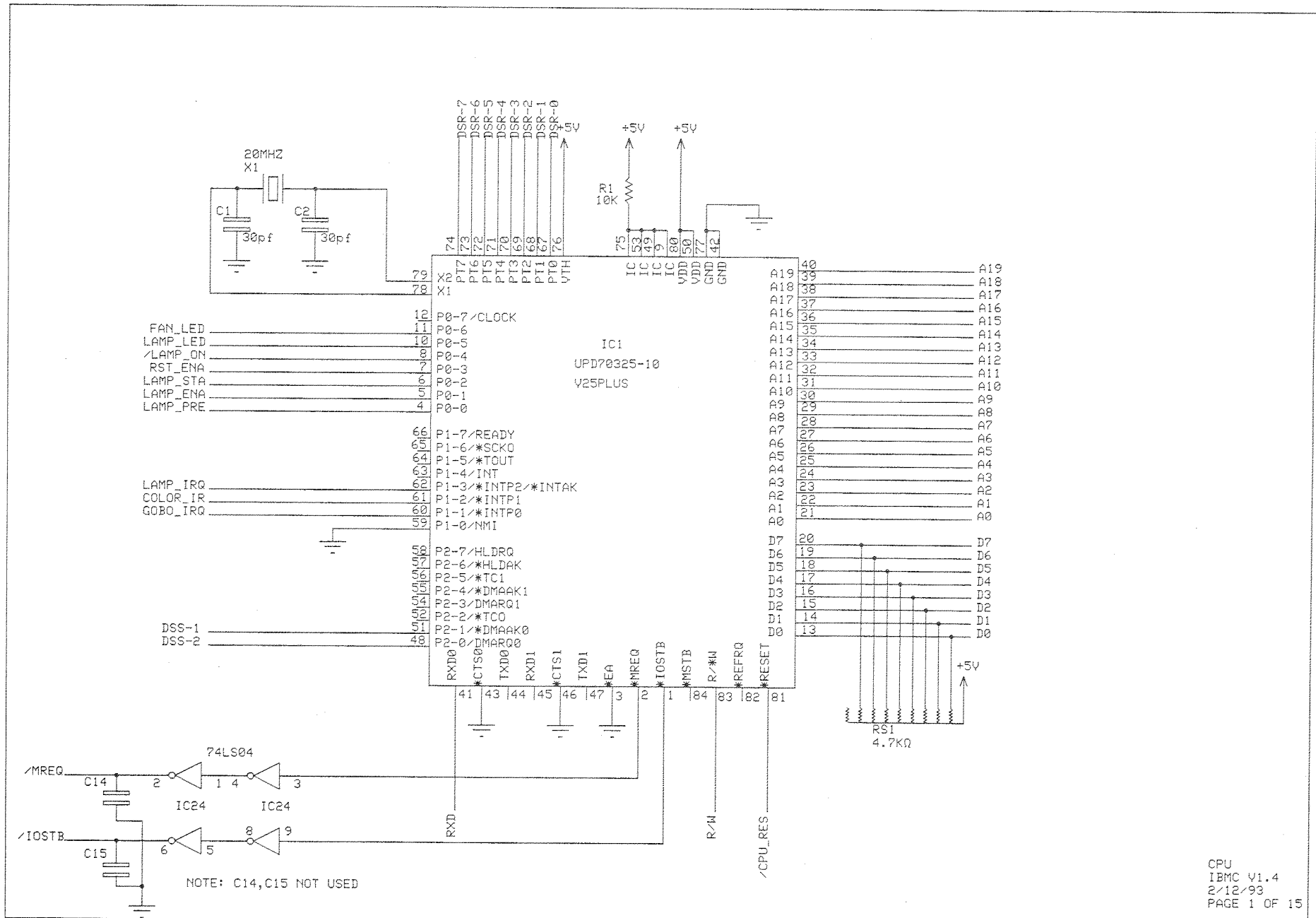
Revision 1.4

List of Schematics

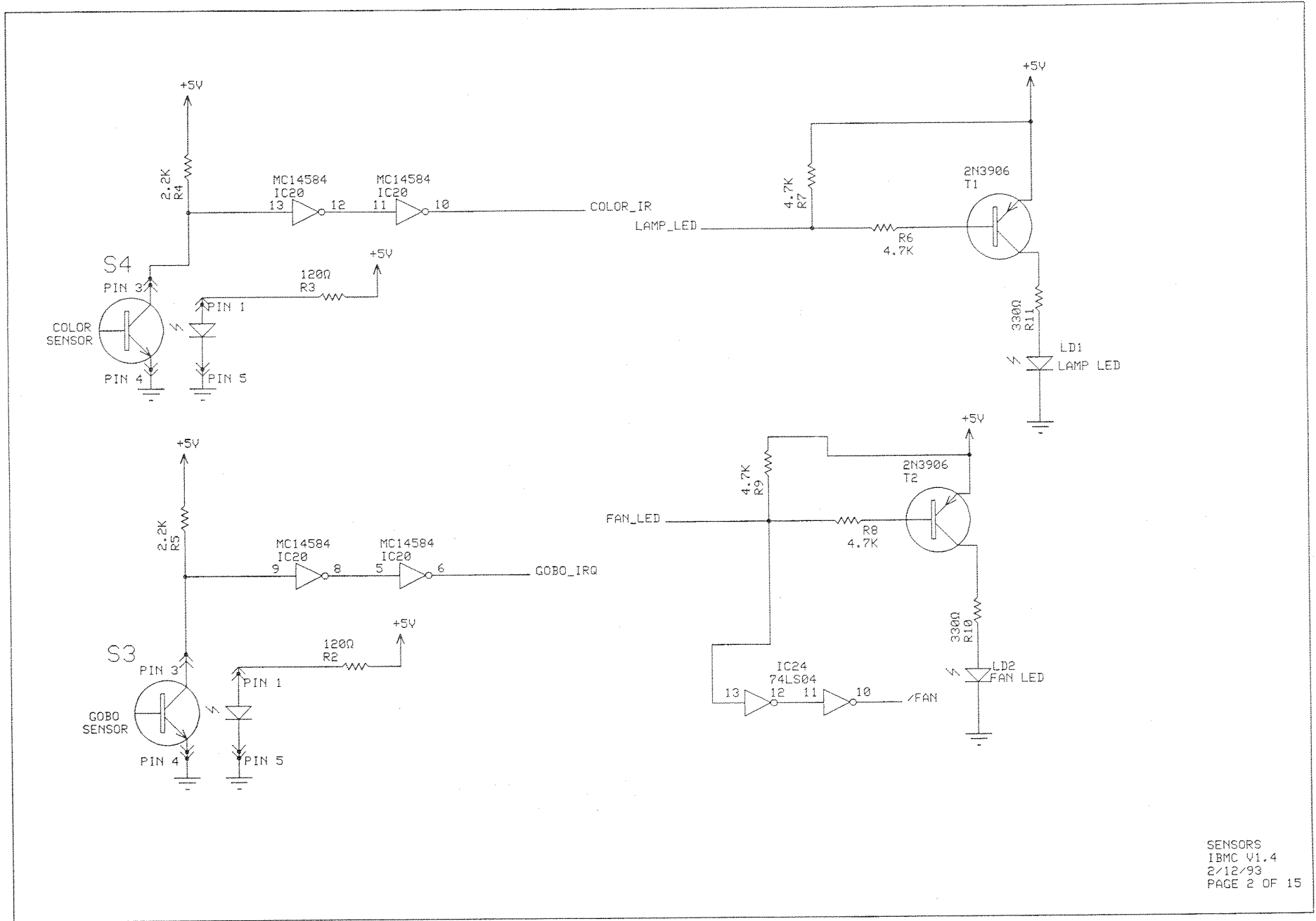
MOTOR/COMM BOARD (V 1.4)	
CPU	1
SENSORS	2
ROM	3
Y AXIS	4
X AXIS	5
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High End Systems, Inc.
2217 West Braker Lane
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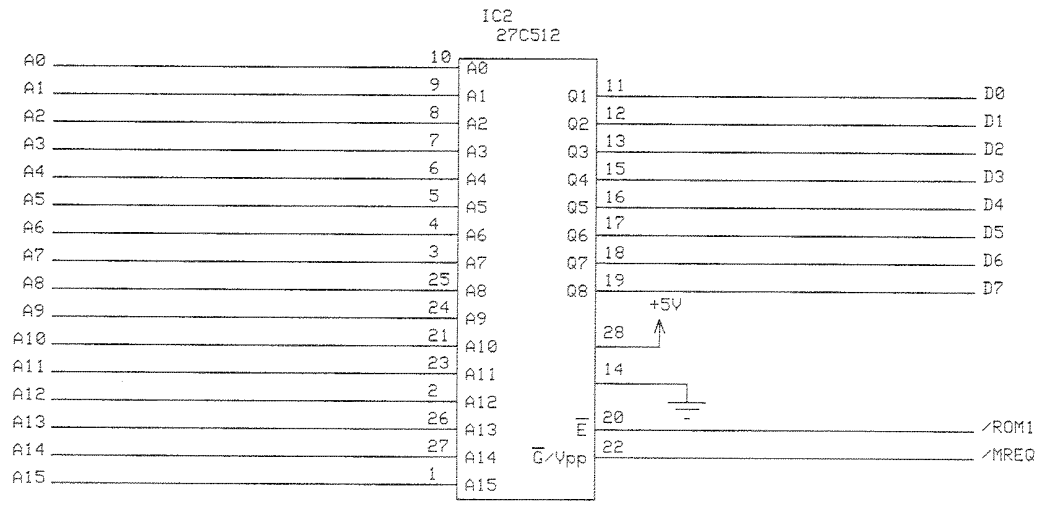
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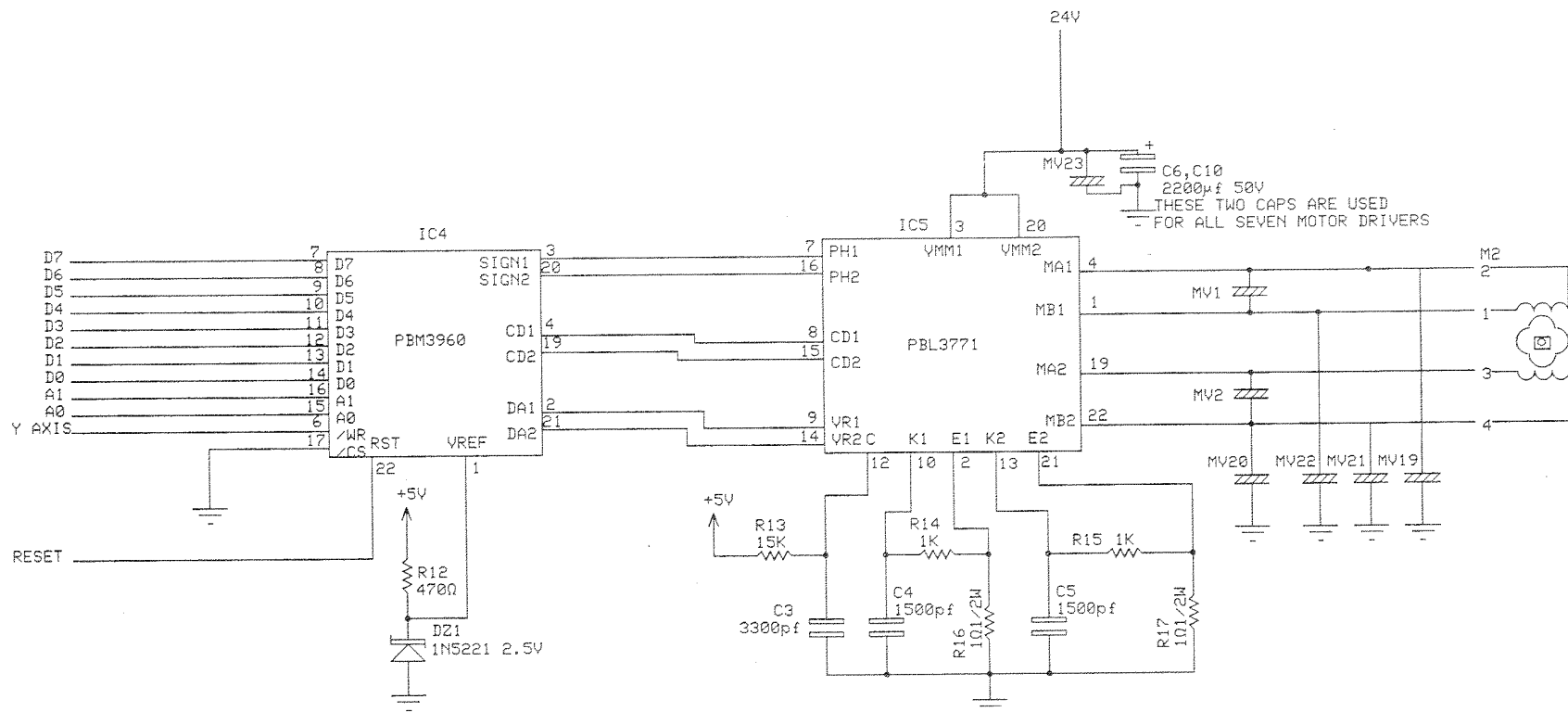
CPU
 IBM V1.4
 2/12/93
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SENSORS
 IBMC V1.4
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ROM
 IBMC V1.4
 2/12/93
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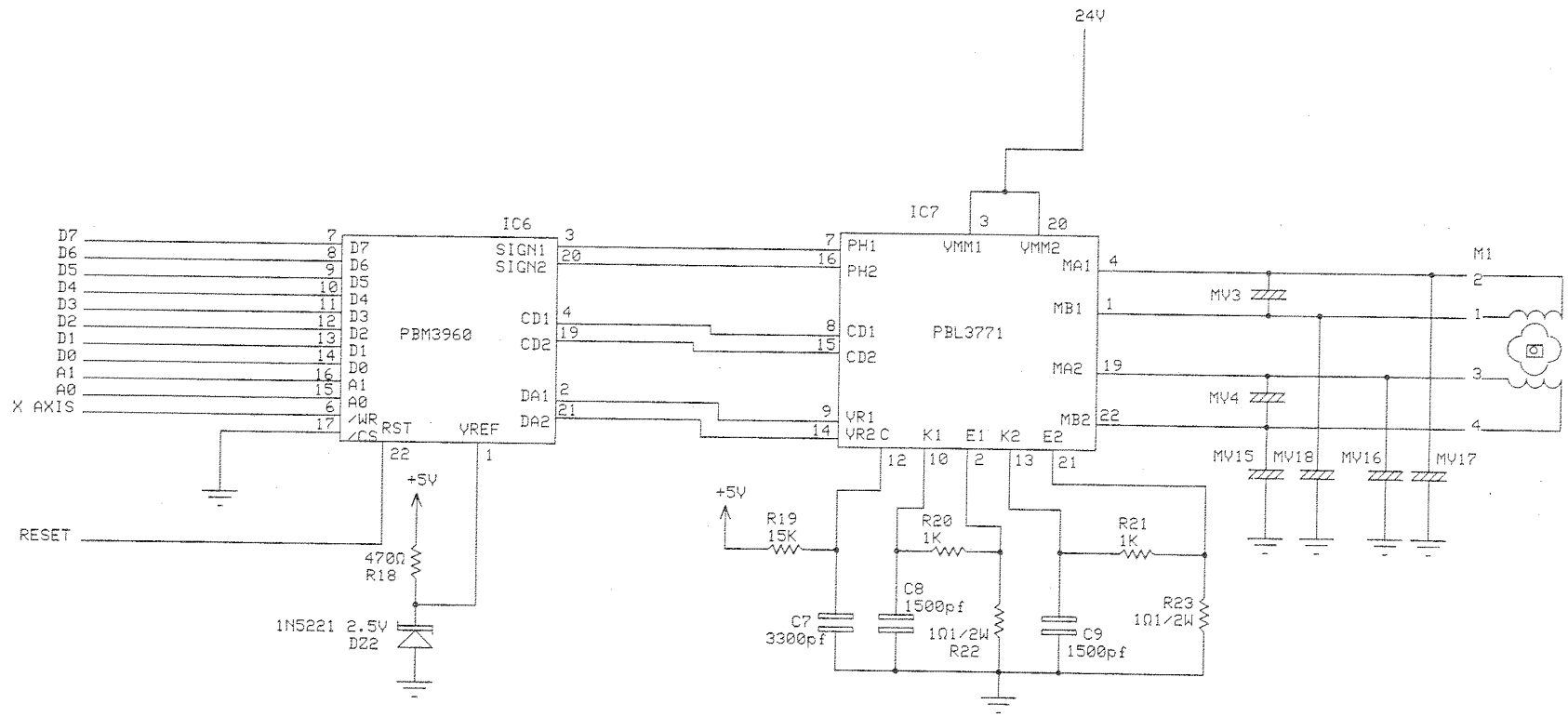


24V

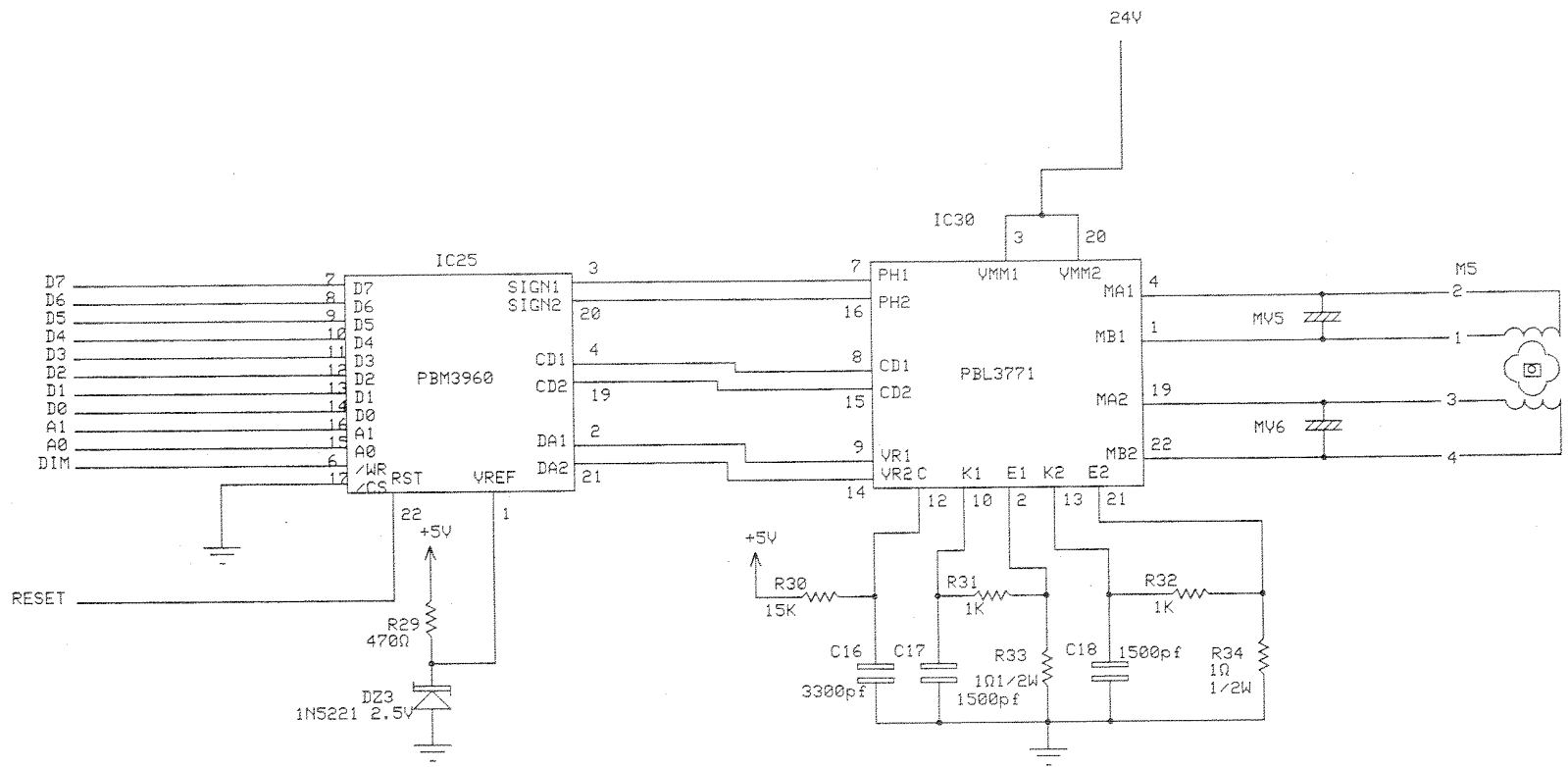
MV23

C6, C10
2200µf 50V
THESE TWO CAPS ARE USED
FOR ALL SEVEN MOTOR DRIVERS

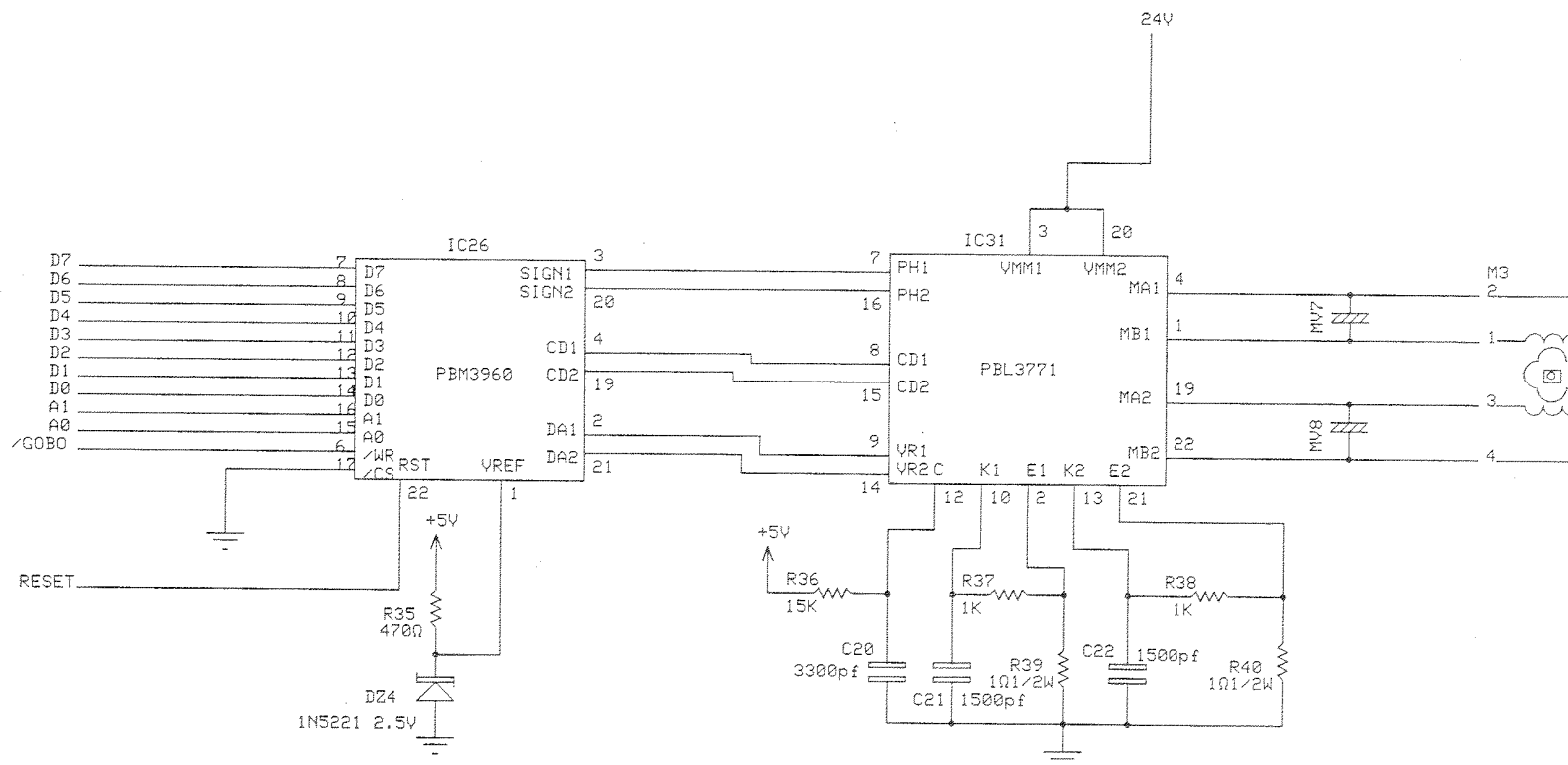
Y AXIS
IBMC V1.4
2/12/93
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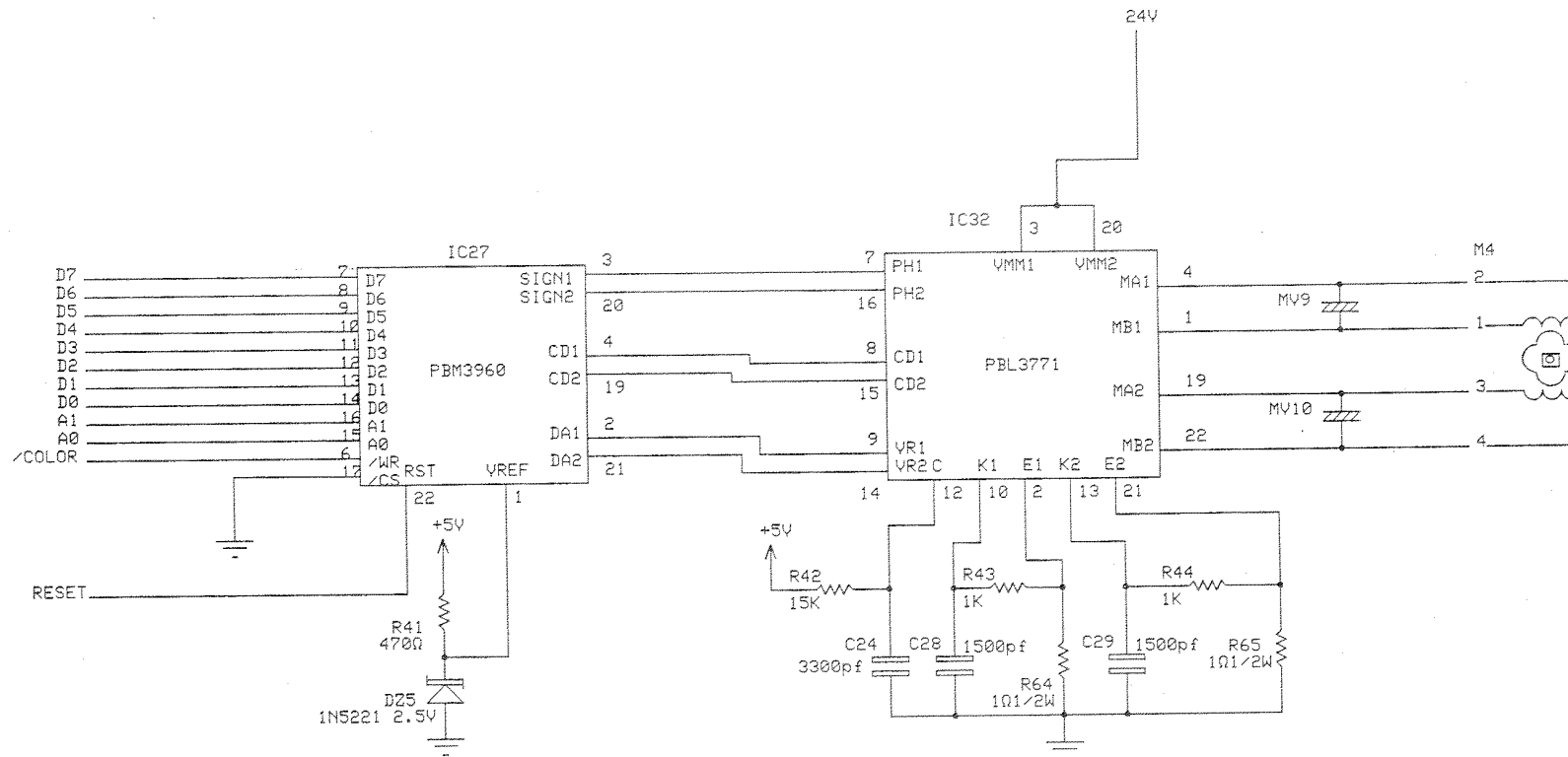
X AXIS
 IBMC V1.4
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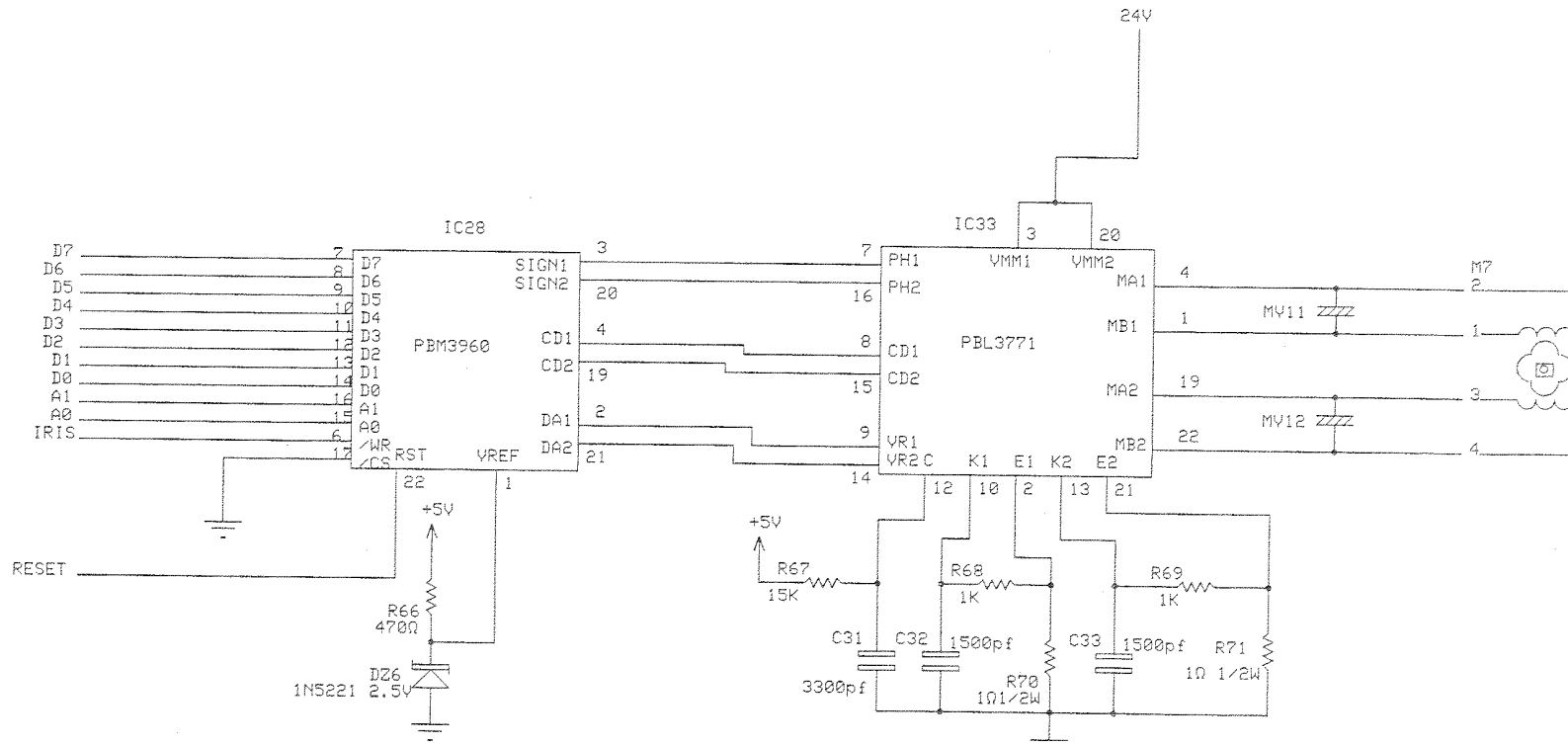
DIM
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 2/12/93
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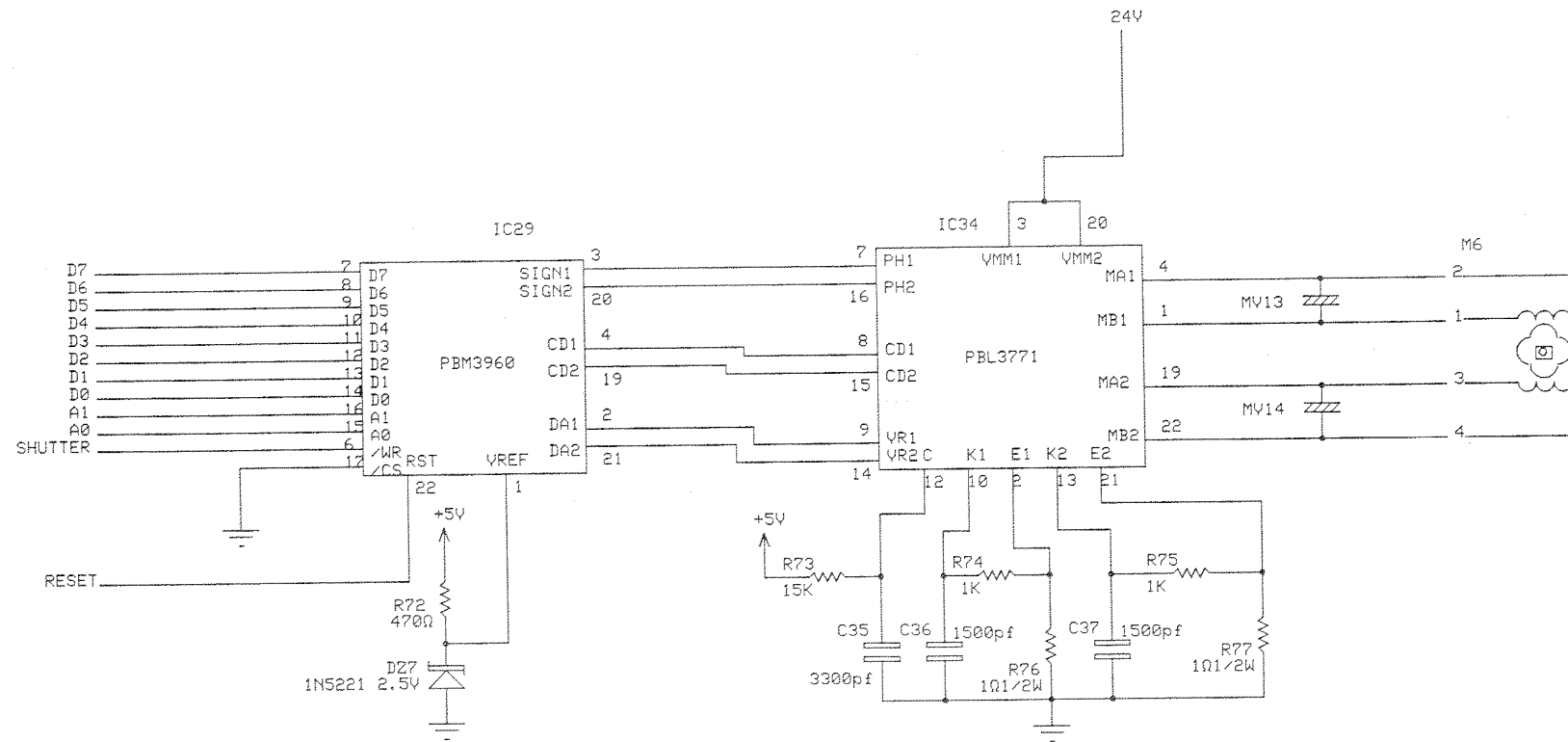


GOBO
 IBMC V1.4
 2/12/93
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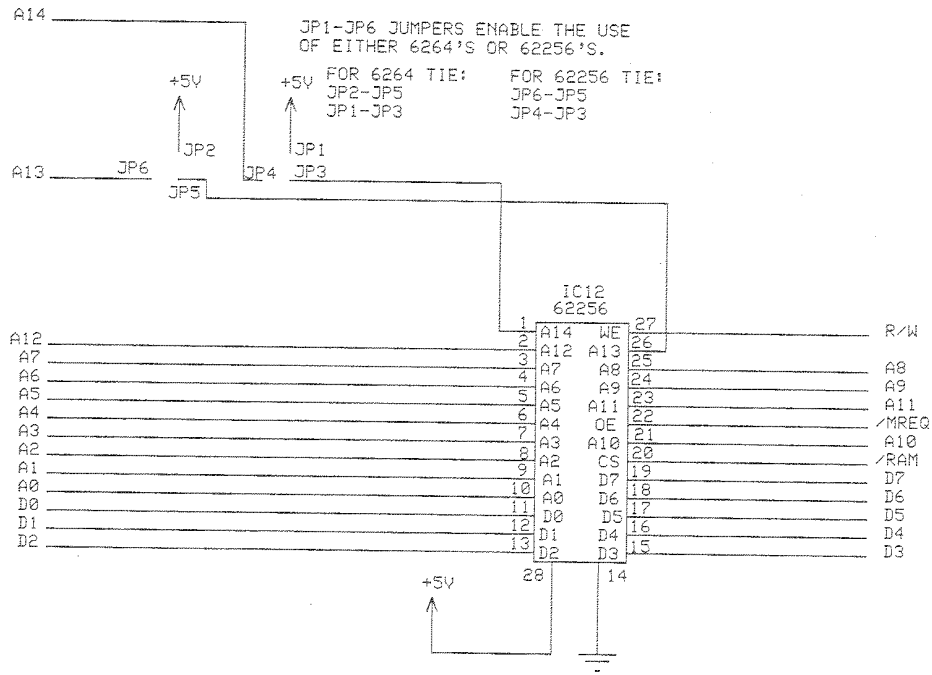


COLOR
 IBMC V1.4
 2/12/93
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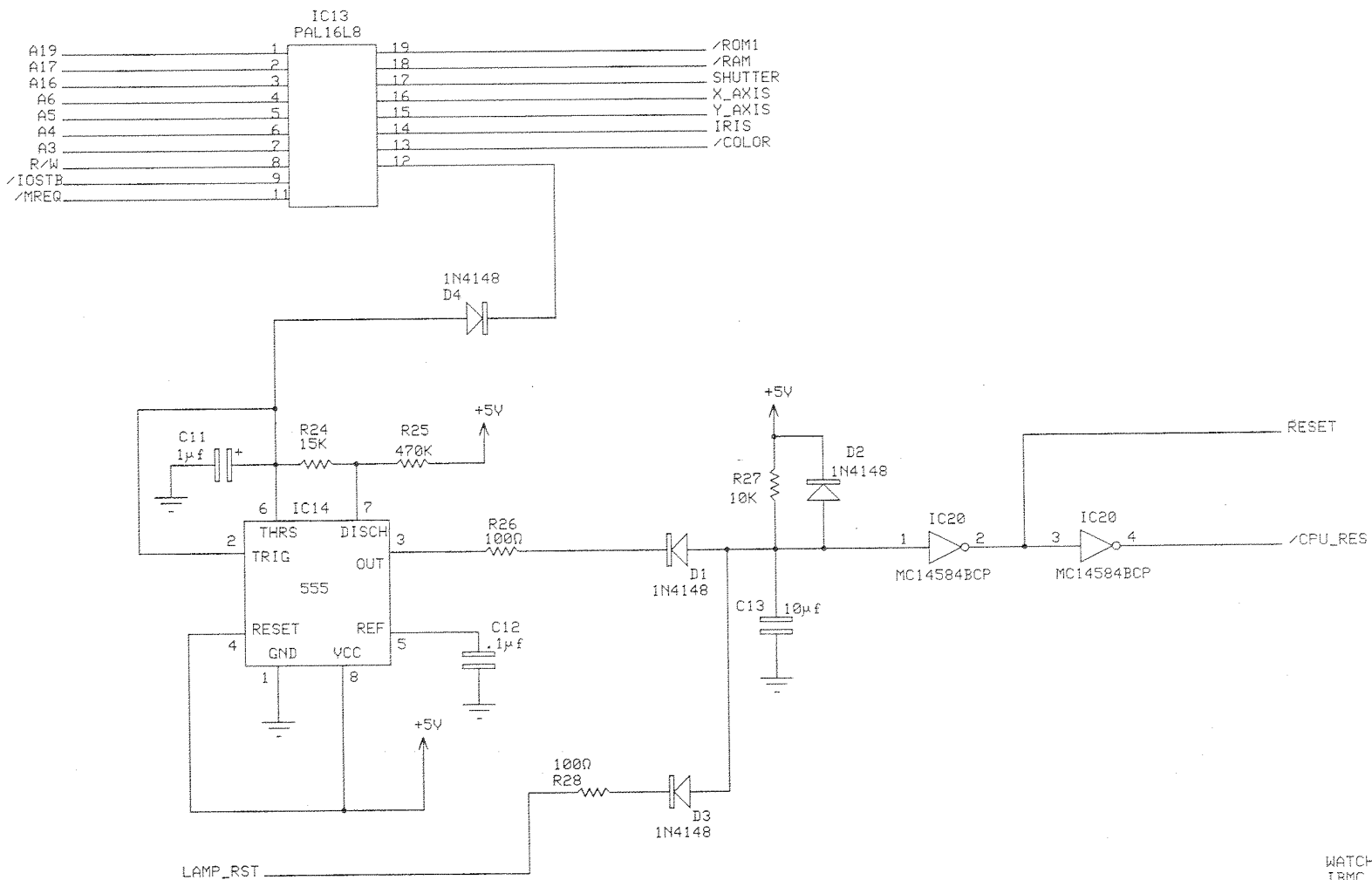




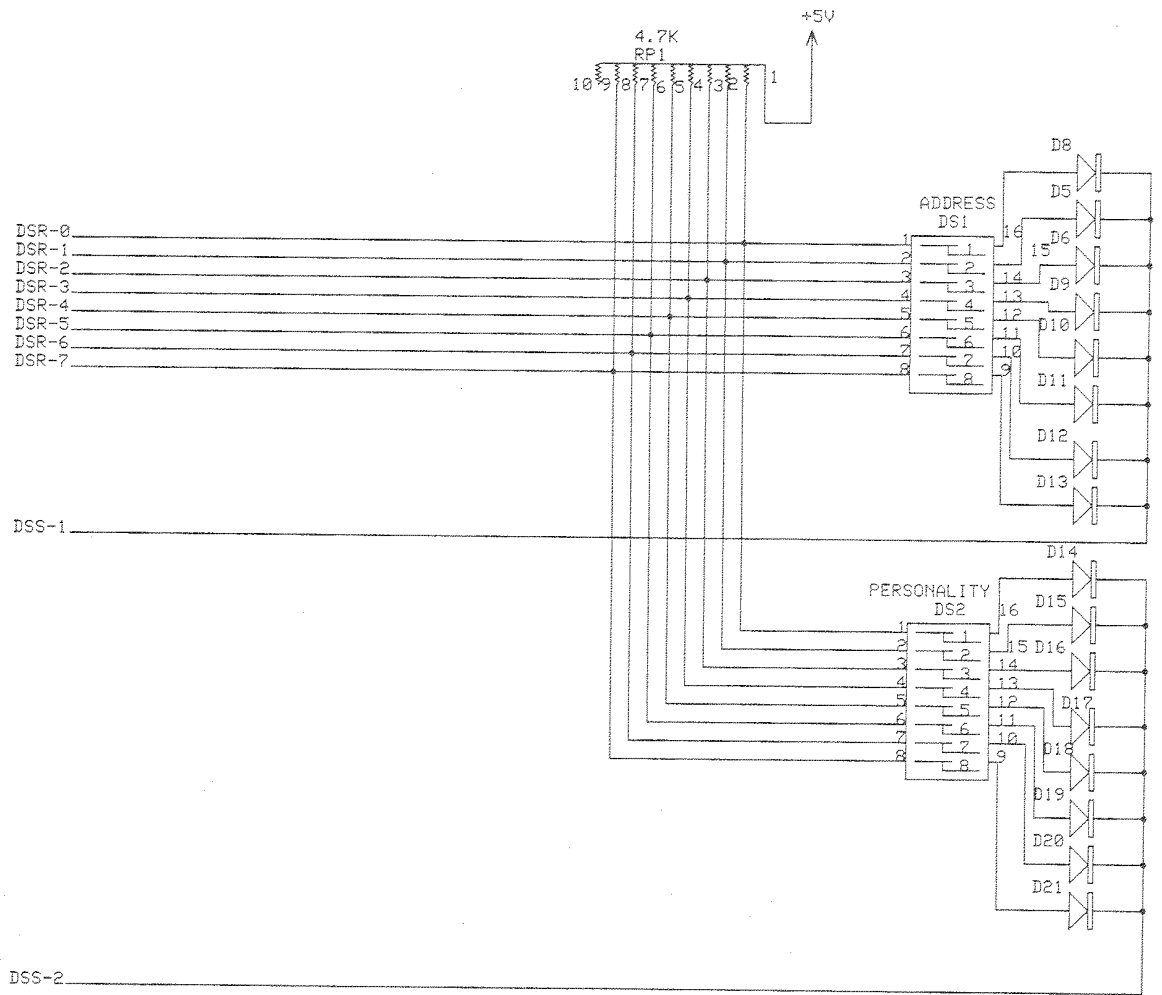
SHUTTER
 IBMC V1.4
 2/12/93
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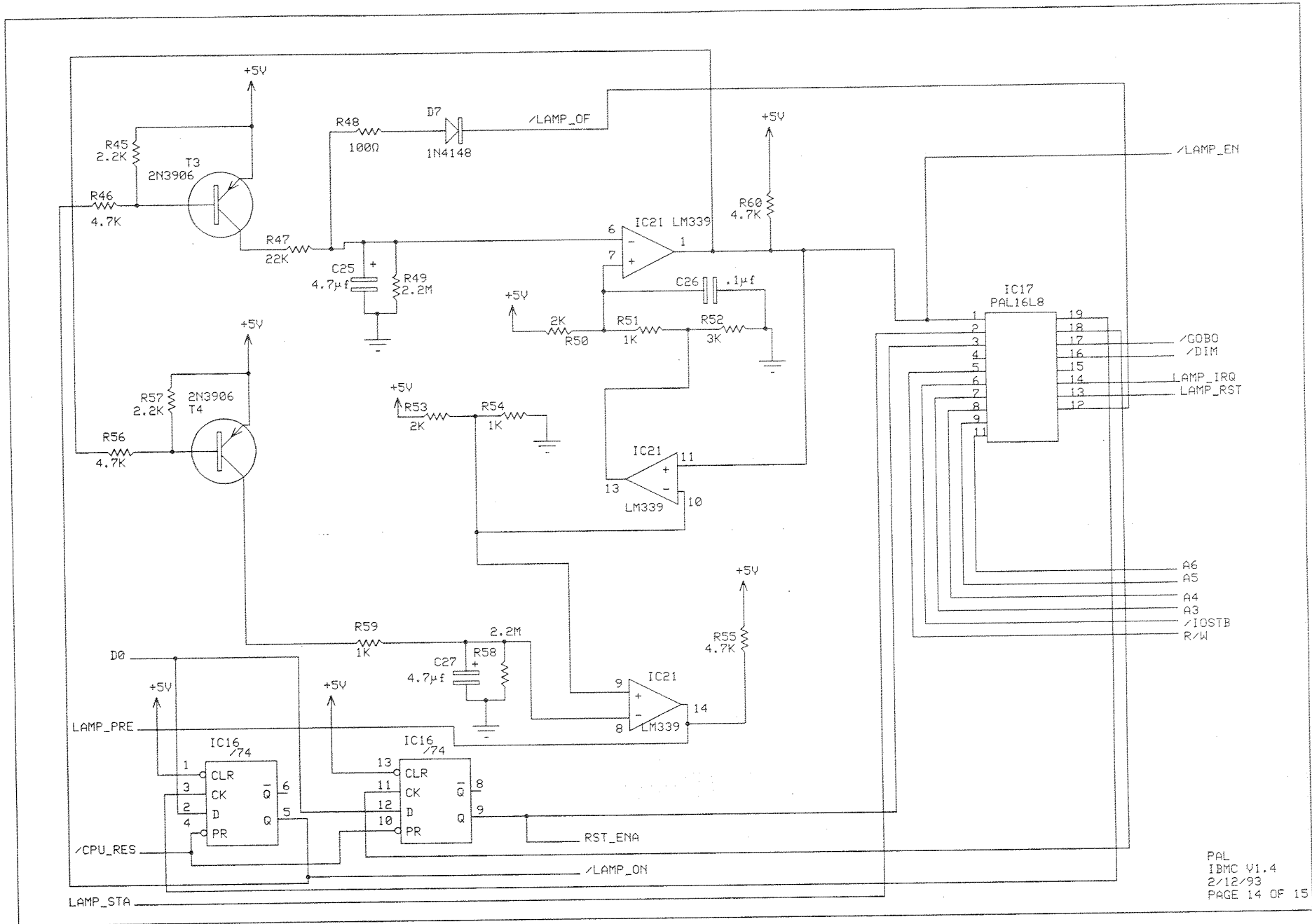
RAM
IBMC V1.4
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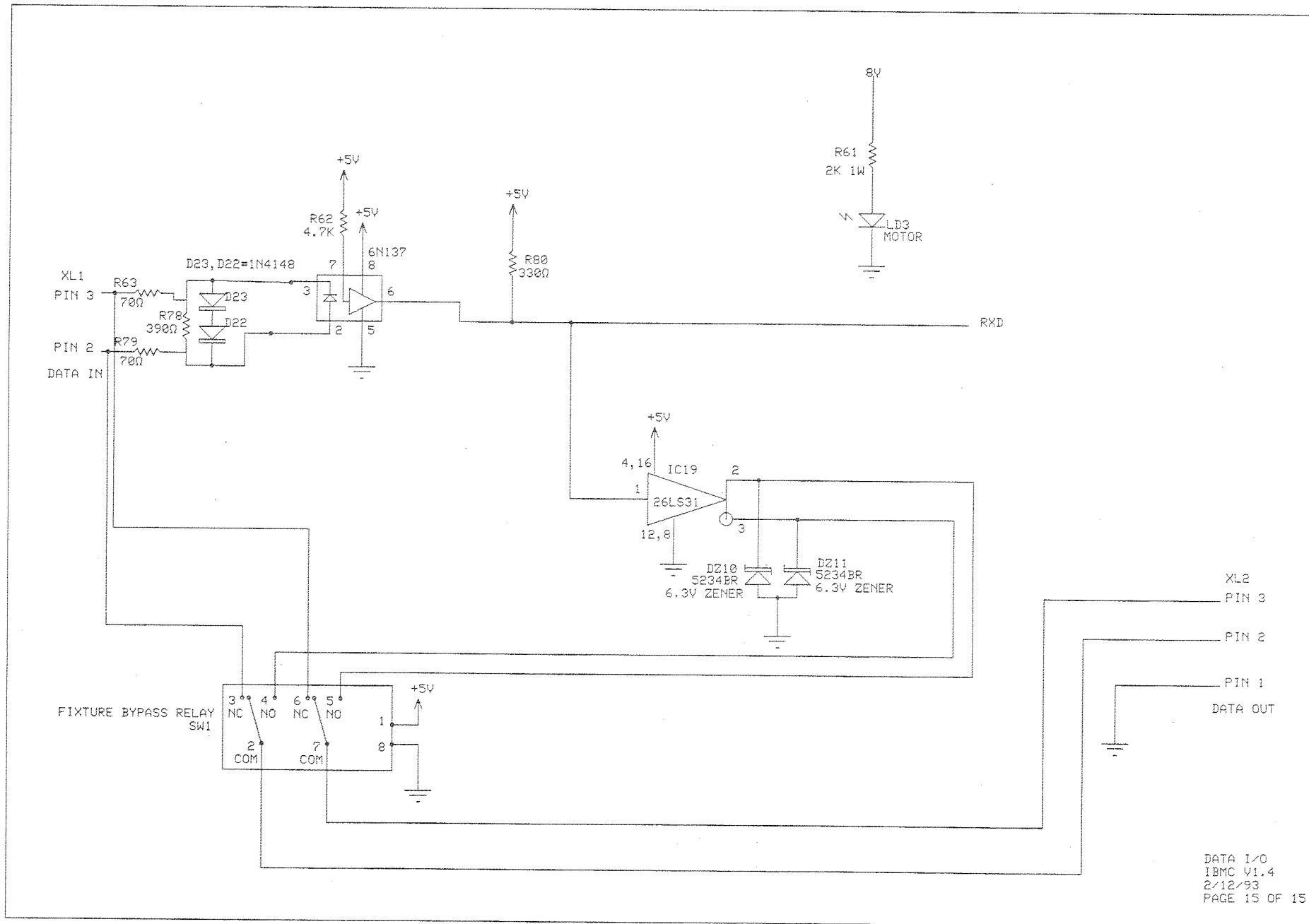
WATCHDOG
IBMC V1.4
2/12/93
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ADDRESS/PERSONALITY
 BMC V1.4
 2/12/93
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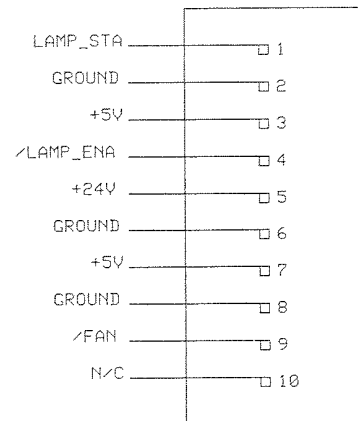


PAL
 IBMC V1.4
 2/12/93
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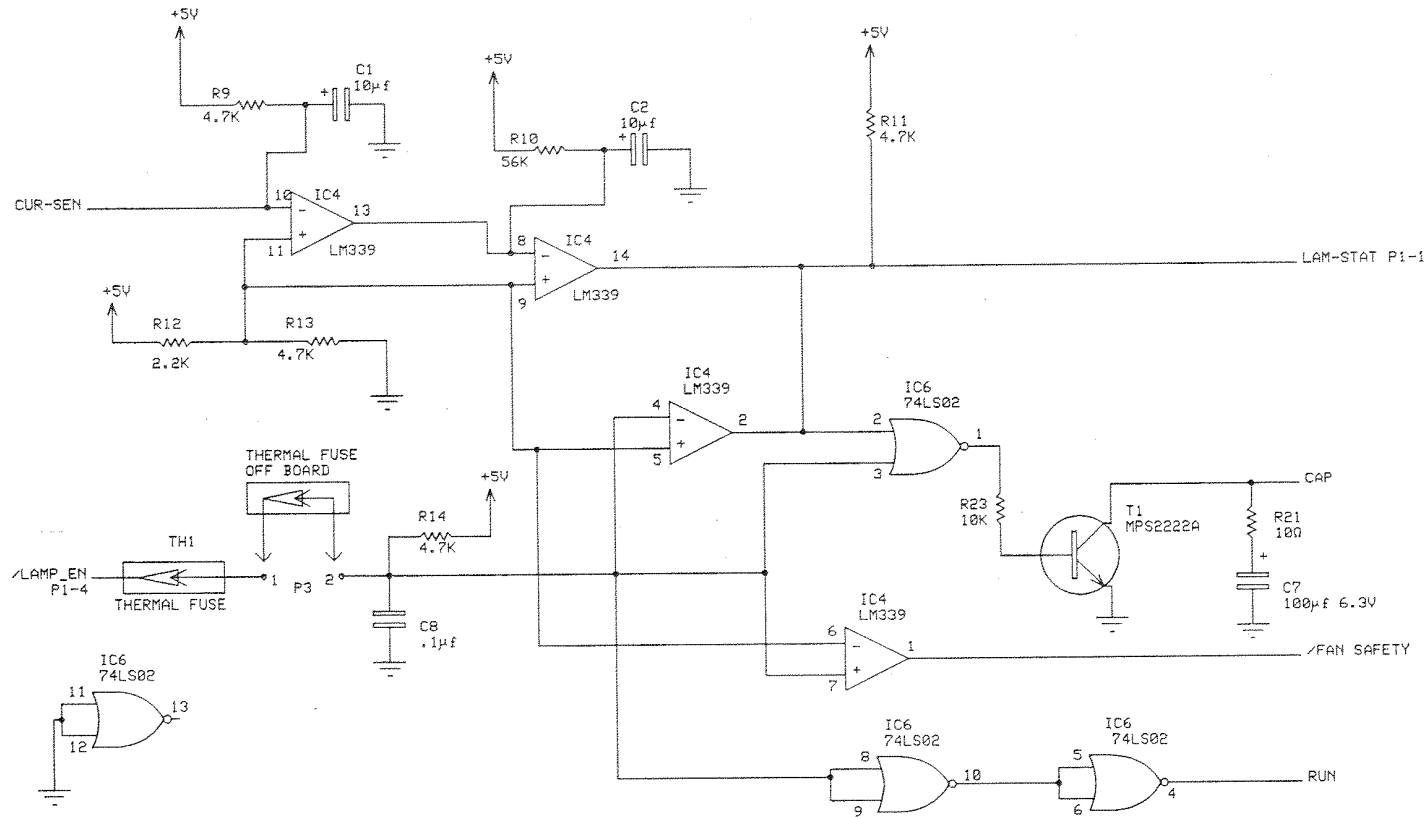


DATA I/O
 IBM C V1.4
 2/12/93
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LIGHTWAVE RESEARCH
INTELLABEAM MAIN FIXTURE BOARD
IBMC CONNECTOR P1 PINOUT



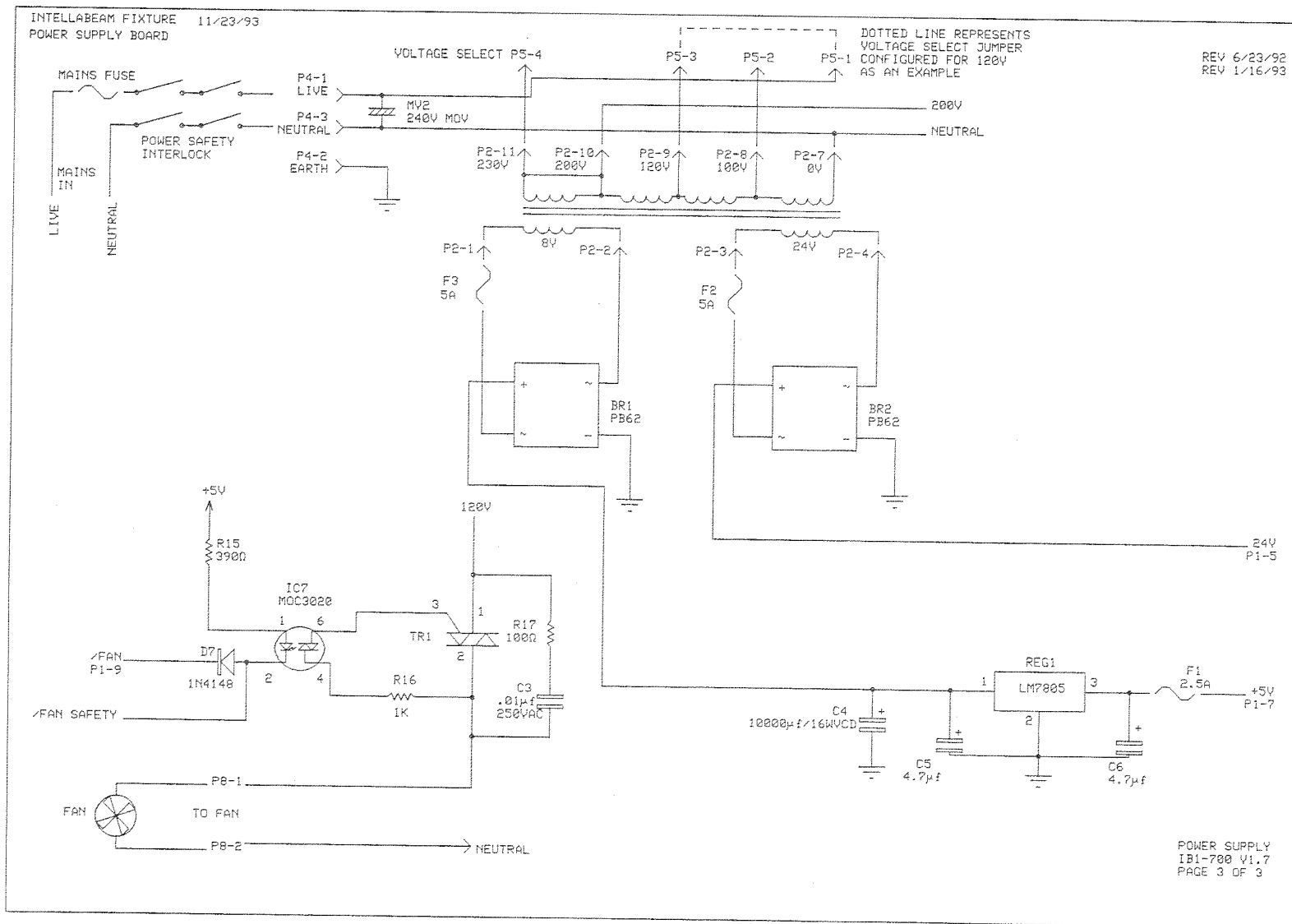
IBMC
P1 PINOUT
PAGE 1 OF 1



IC4 (LM339) PIN 3=V+ AND PIN 12=GND
IC6 (74LS02) PIN 14=V+ AND PIN 7=GND

CONNECTOR P1
PIN 2, 6 AND 8 ARE CONNECTED TO GROUND

POWER SUPPLY
IB1-700 V1.7
PAGE 2 OF 3



REVISION HISTORY

SCH REV	PCB REV	ECO NO.	DATE	COMMENT
- 4.1	- 4.1	-	02/06/97	INITIAL

NOTES:


UNLESS OTHERWISE SPECIFIED:

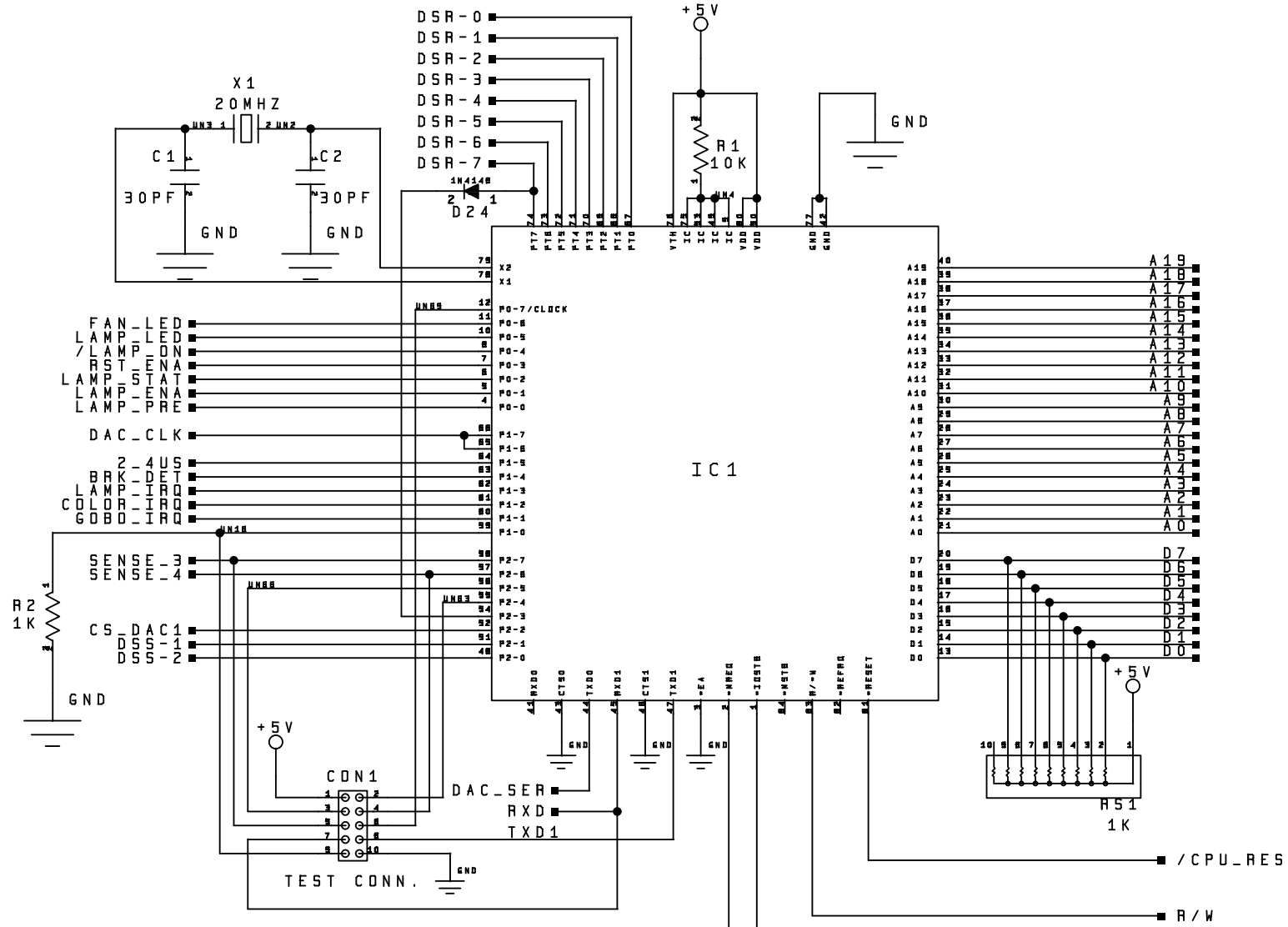
1. ALL RESISTOR VALUES ARE IN OHMS
2. ALL RESISTORS ARE 1/4 WATT 5% OR BETTER
3. CAPACITOR VALUES ARE IN PICO FARADS
4. ALL NON POLARIZED CAPACITORS ARE 50V OR BETTER
VALUES BELOW 470pF ARE NPO/COG TYPE 5%
VALUES 470pF TO 1000pF ARE X7R 10%
VALUES ABOVE 1000pF ARE Z5U +/- 20 %
5. ALL POLARIZED CAPACITORS ARE 20% OR BETTER
6. ALL DIODES ARE 1N4148

INTELLABEAM LOGIC BOARD

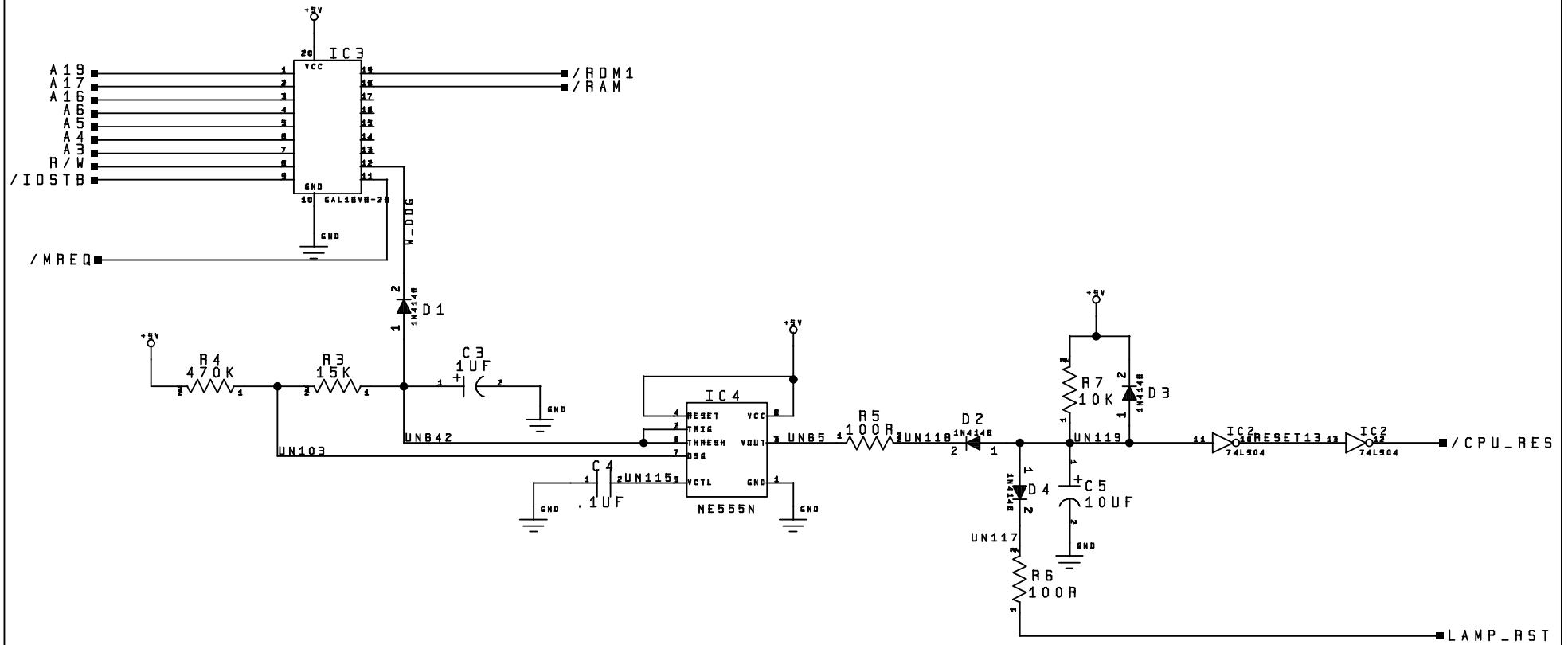
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HIGH END SYSTEMS INC.			
Title: INTELLABEAM LOGIC BOARD CPU-MAIN			
Function: COVER SHEET 081R41			
Drawing No. SCH-99030081	REV. 4.1	Designed: TOM GRIMES	
Drawn: DERRICK RIVERS		Approved:	
Date 8-9-1997-15:28	Size A2	Page	1 of 12



HIGH END SYSTEMS INC.			
Title:		INTELLABEAM LOGIC BOARD CPU-MAIN	
Function:		CPU	081R41
Drawing No.		REV.	Date
SCH-990300B1		4.1	3-21-1997-10.32
Size		Page	
A2		2 of 12	



HIGH END SYSTEMS INC.

Title: INTELLABEAM LOGIC BOARD
CPU-MAIN

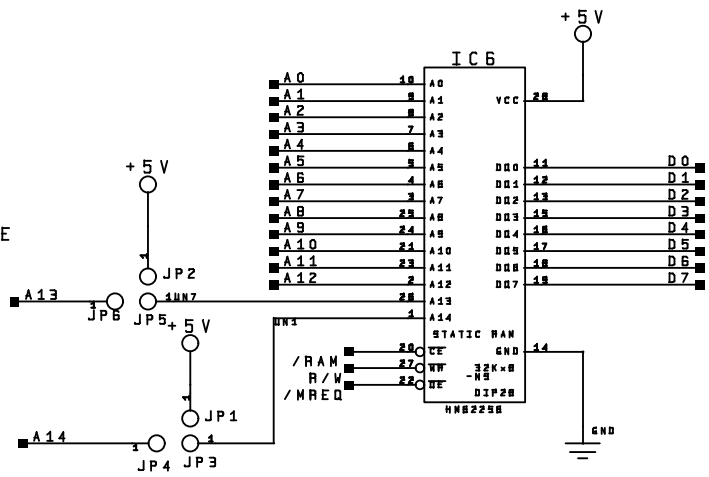
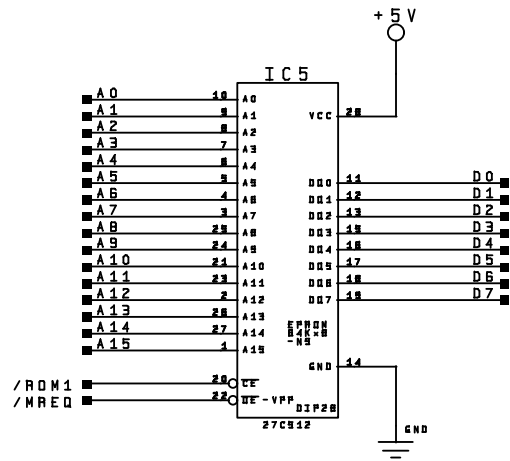


Function: WATCHDOG 081R41

Drawing No.
SCH-990300B1

REV.
4.1
Size A2

Date 3-21-1997-10-33
Page 3 of 12



JP1-JP6 JUMPERS ENABLE THE USE OF EITHER 6264'S OR 62256'S.

FOR 6264'S TIE:
JP1-JP3
JP2-JP5

FOR 62256 TIE:
JP3-JP4
JP5-JP6

HIGH END SYSTEMS INC.

Title: INTELLABEAM LOGIC BOARD
CPU-MAIN



Function: RDM & RAM 081R41

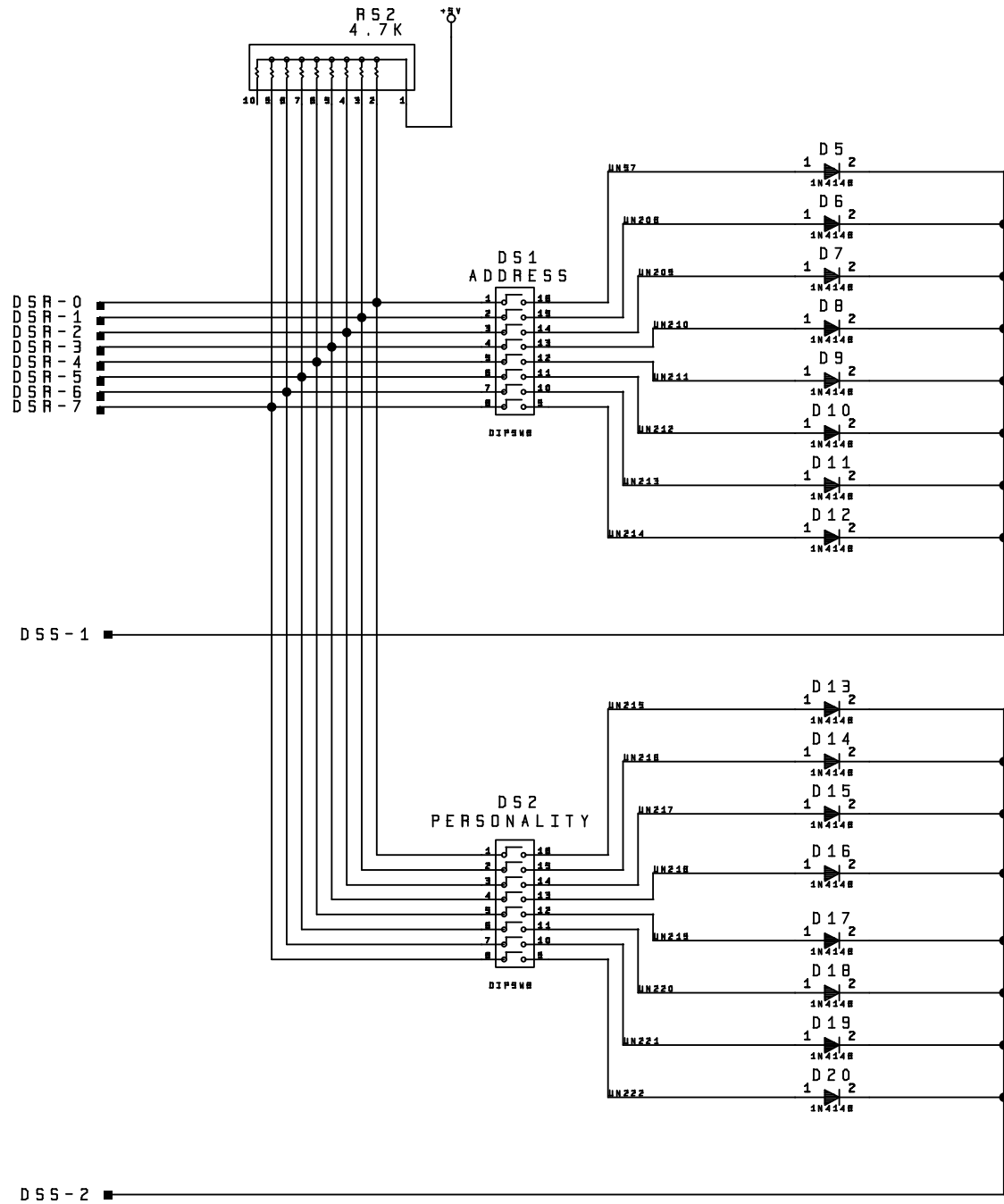
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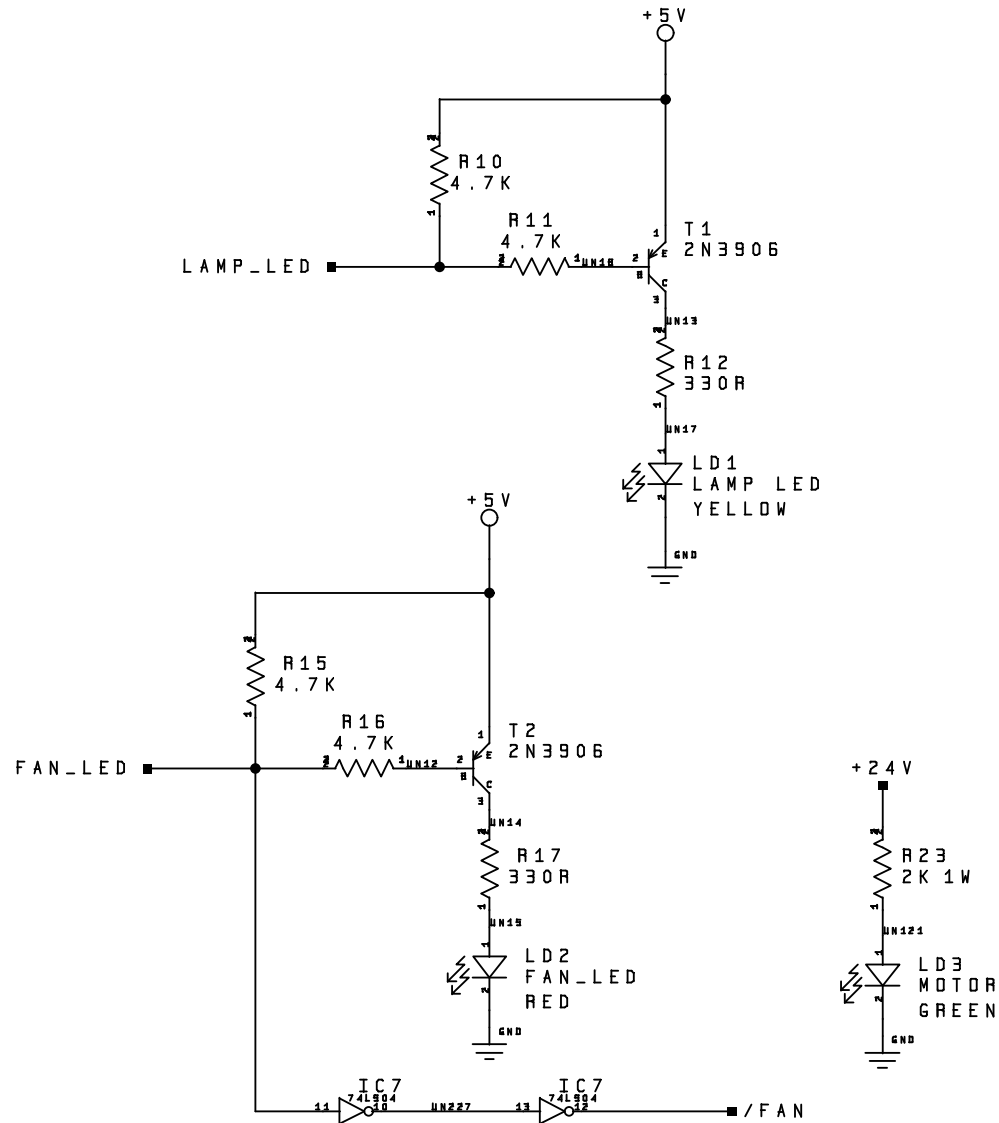
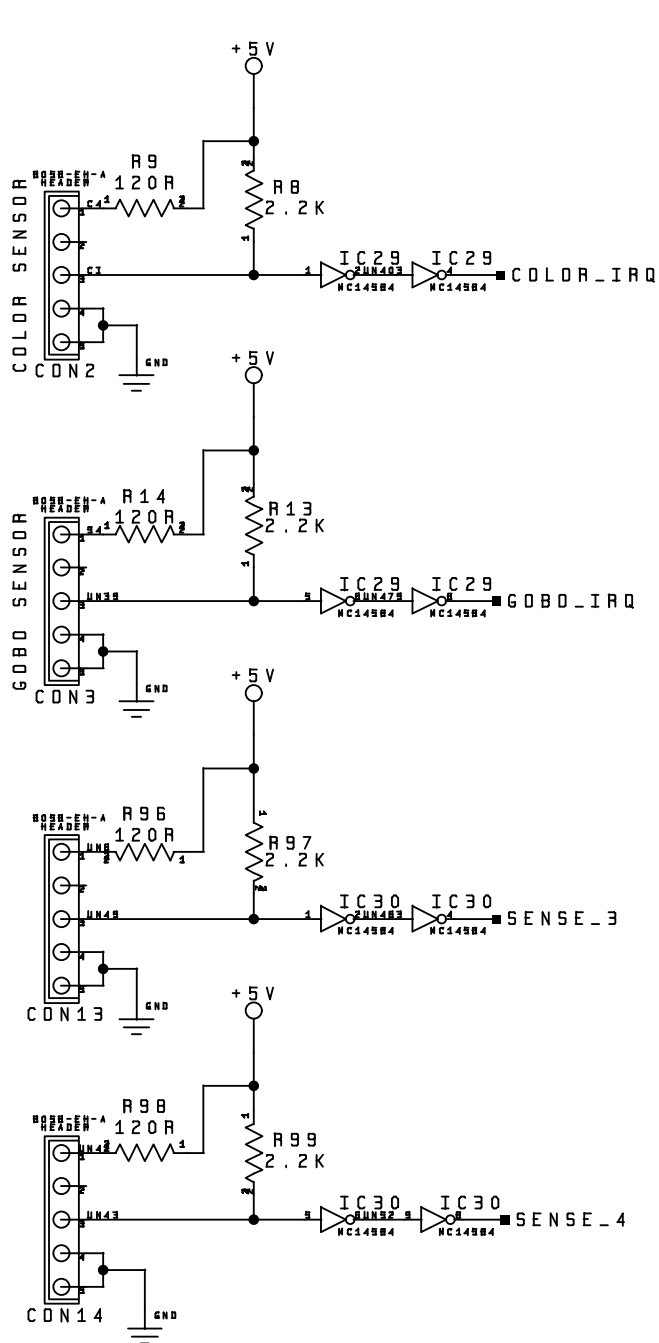
Size A2

Date 3-21-1997-10-34

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Drawing No.	REV.	Date	3-21-1997-10-34
SCH-990300B1	4.1	Size A2	Page 5 of 12



HIGH END SYSTEMS INC.

Title: INTELLABEAM LOGIC BOARD
 CPU-MAIN

Function: SENSORS 081R41

Drawing No. SCH-990300B1

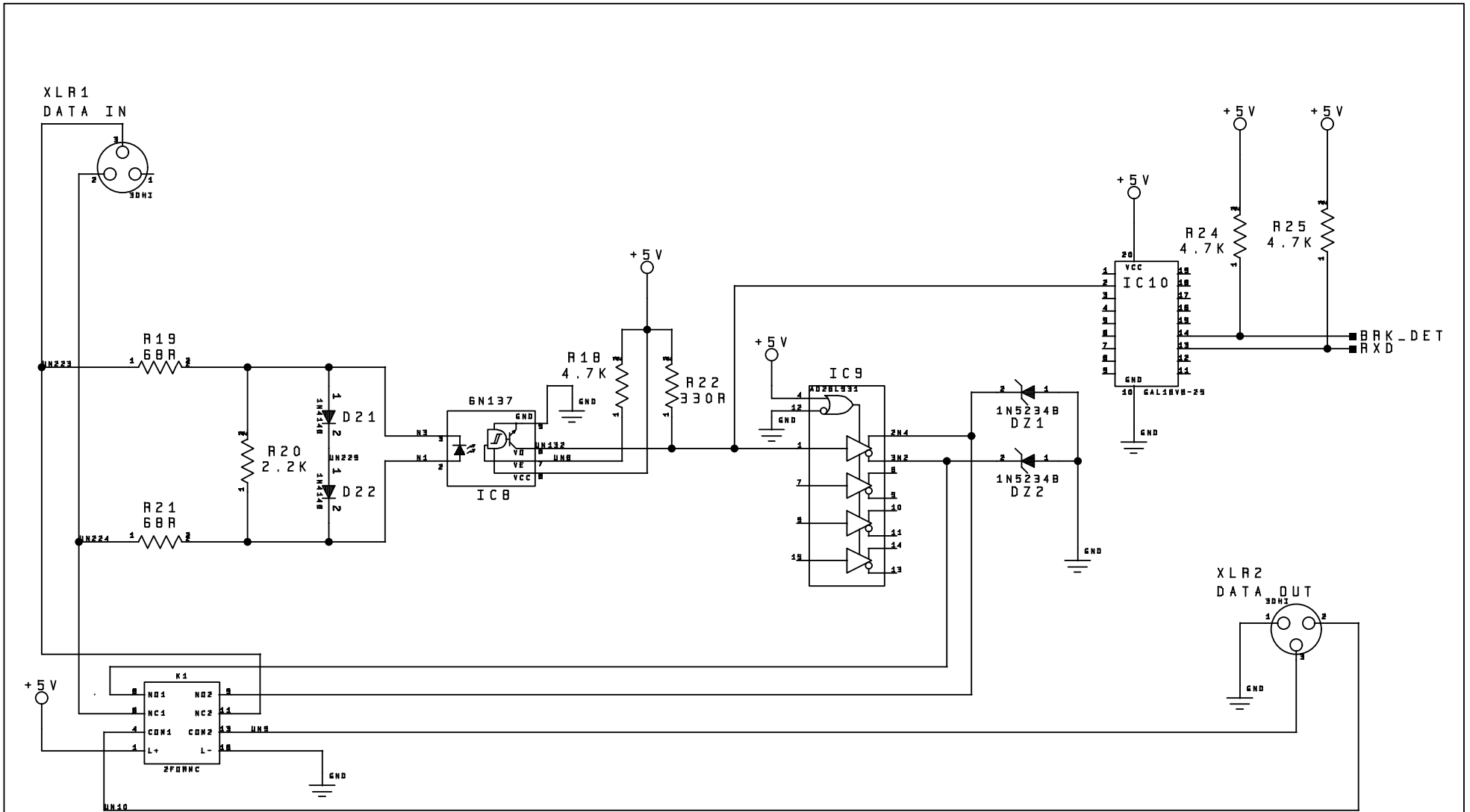
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Size A2

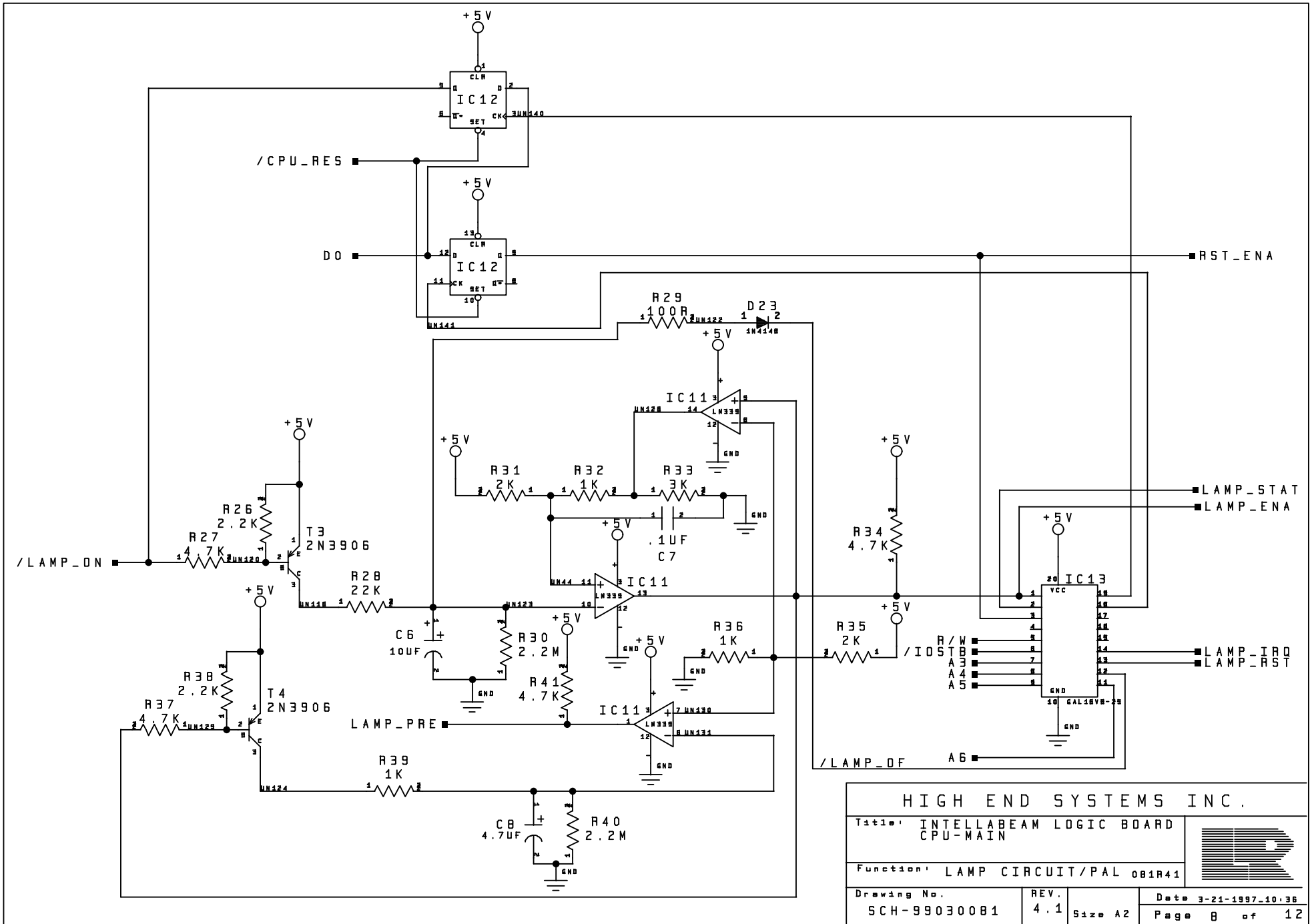
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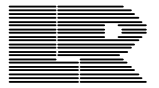
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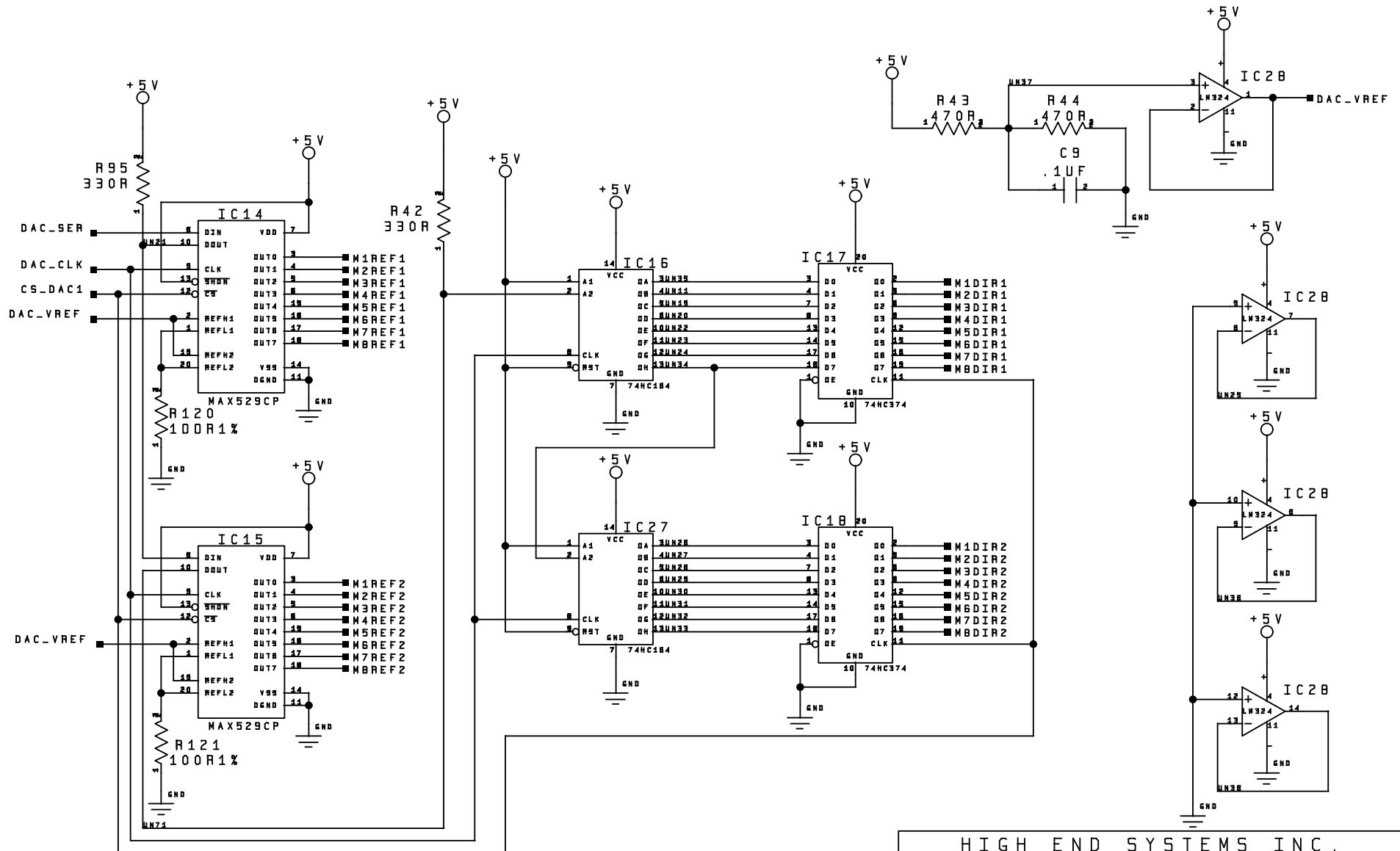




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Size A2			



HIGH END SYSTEMS INC.			
Title: INTELLABEAM LOGIC BOARD CPU-MAIN			
Function: LAMP CIRCUIT/PAL 081R41			
Drawing No. SCH-990300B1	REV. 4.1	Size A2	Date 3-21-1997-10-36 Page 8 of 12



HIGH END SYSTEMS INC.

Title: INTELLABEAM LOGIC BOARD

CPU-MAIN

Function: D/A CONVERTERS 081R41



Drawing No. SCH-990300B1

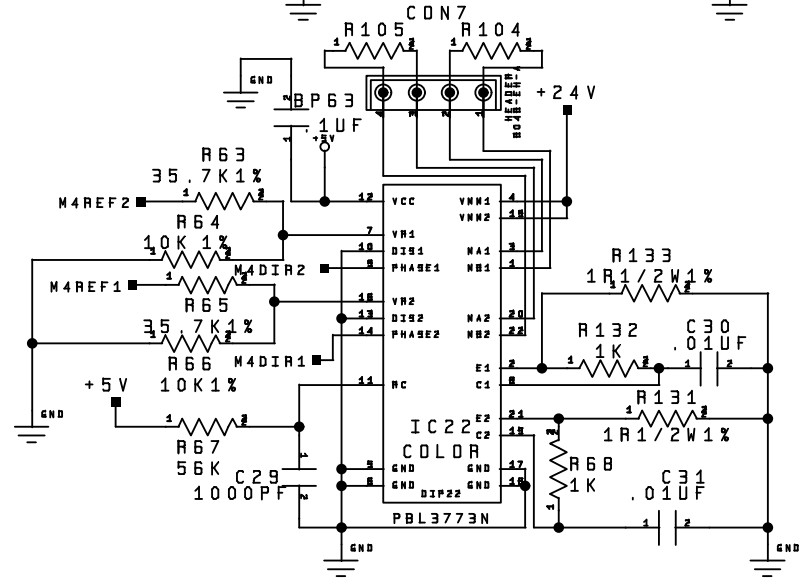
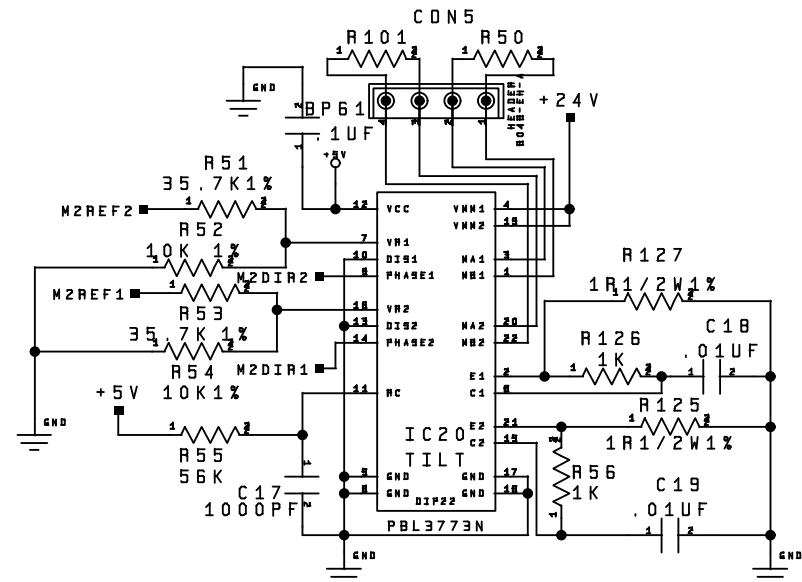
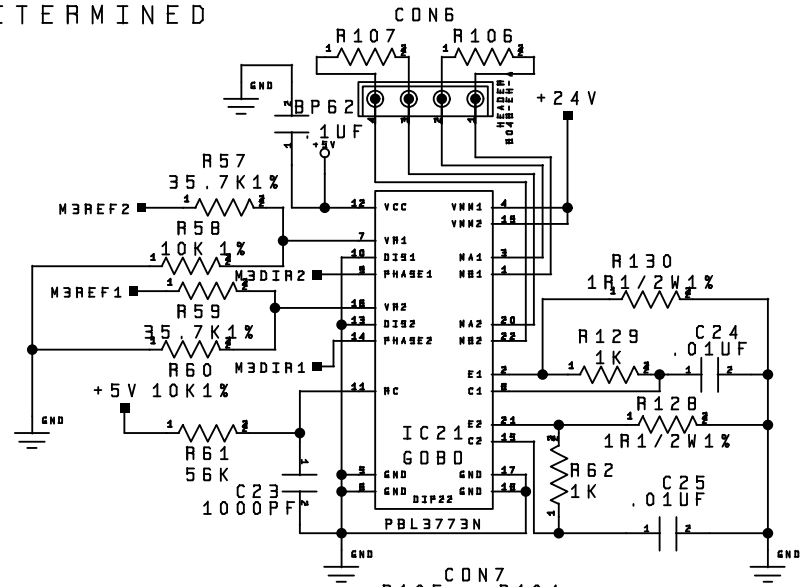
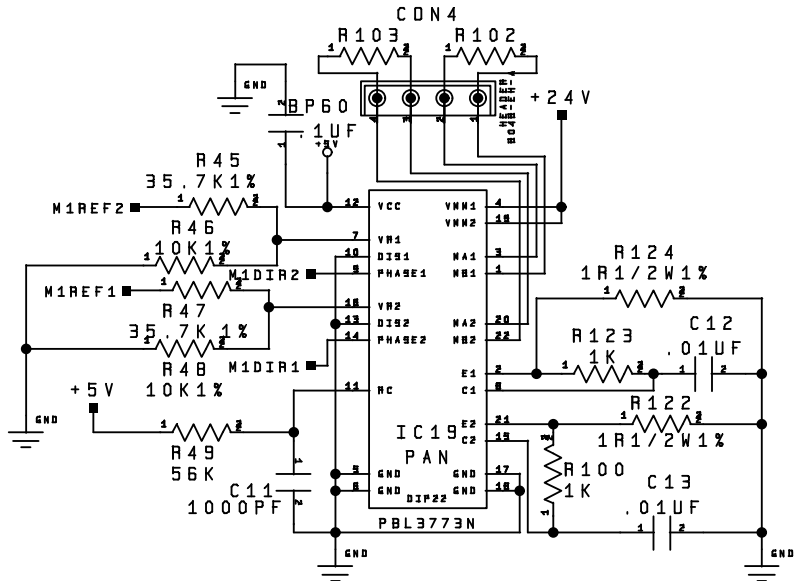
REV. 4.1

Size A2

Date 3-21-1997-10-36

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RESISTOR VALUES ON CON 4,5,6,7 ARE TO BE DETERMINED



HIGH END SYSTEMS INC.

Title: INTELLABEAM LOGIC BOARD
CPU-MAIN



Function: MOTOR DRIVERS 081R41

Drawing No. SCH-990300B1

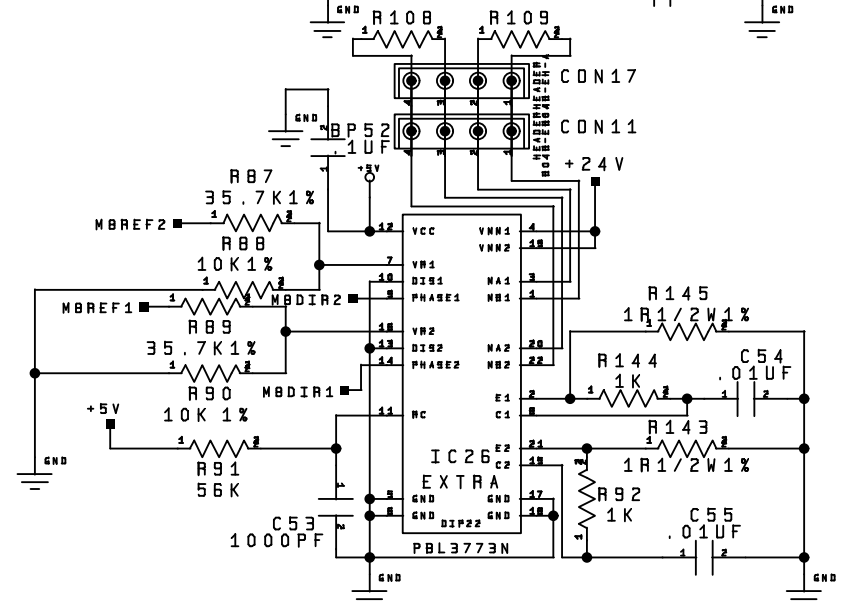
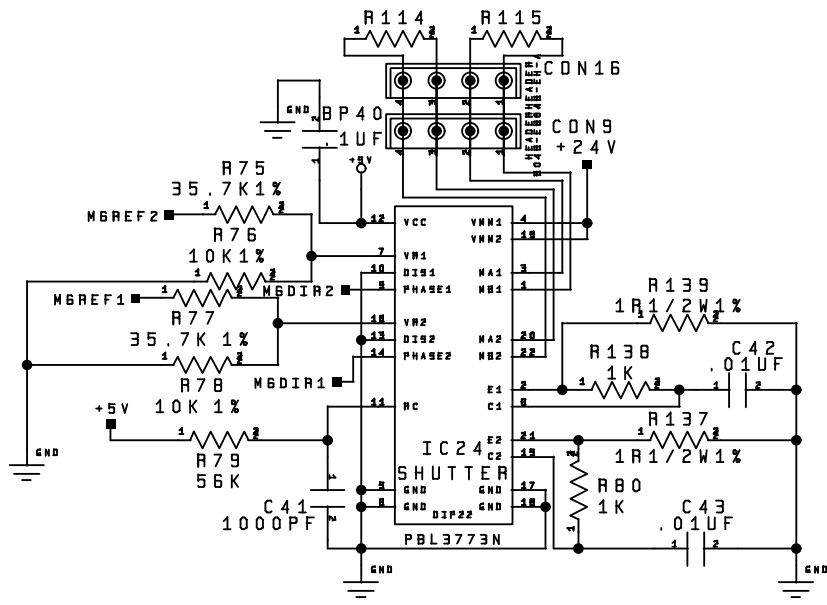
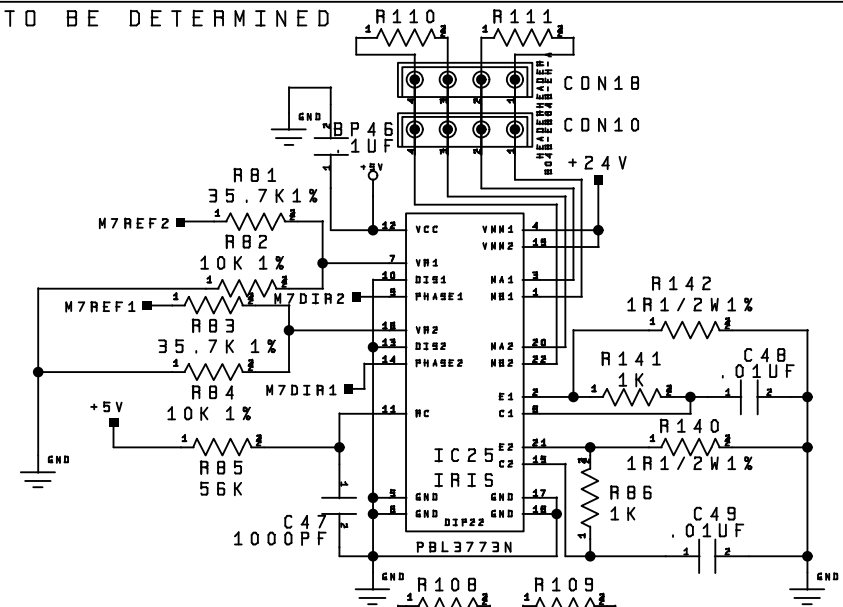
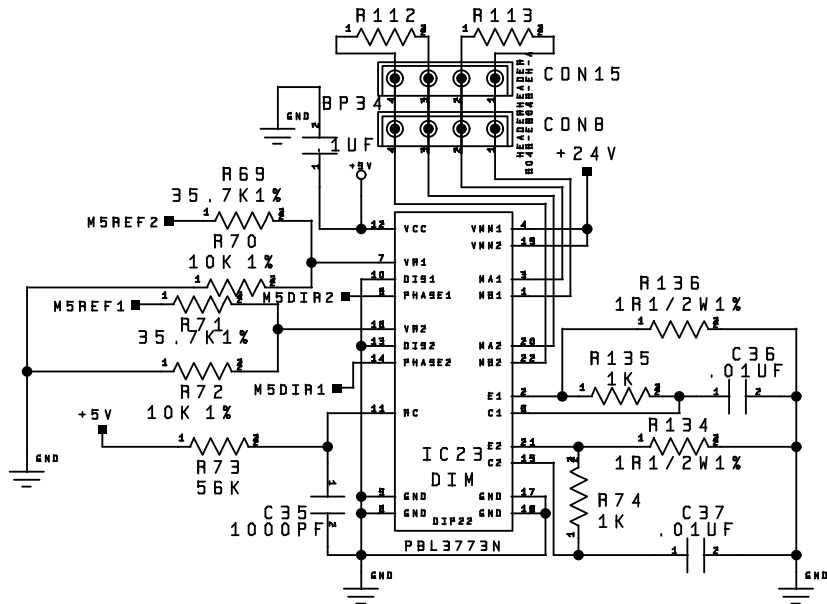
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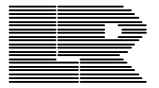
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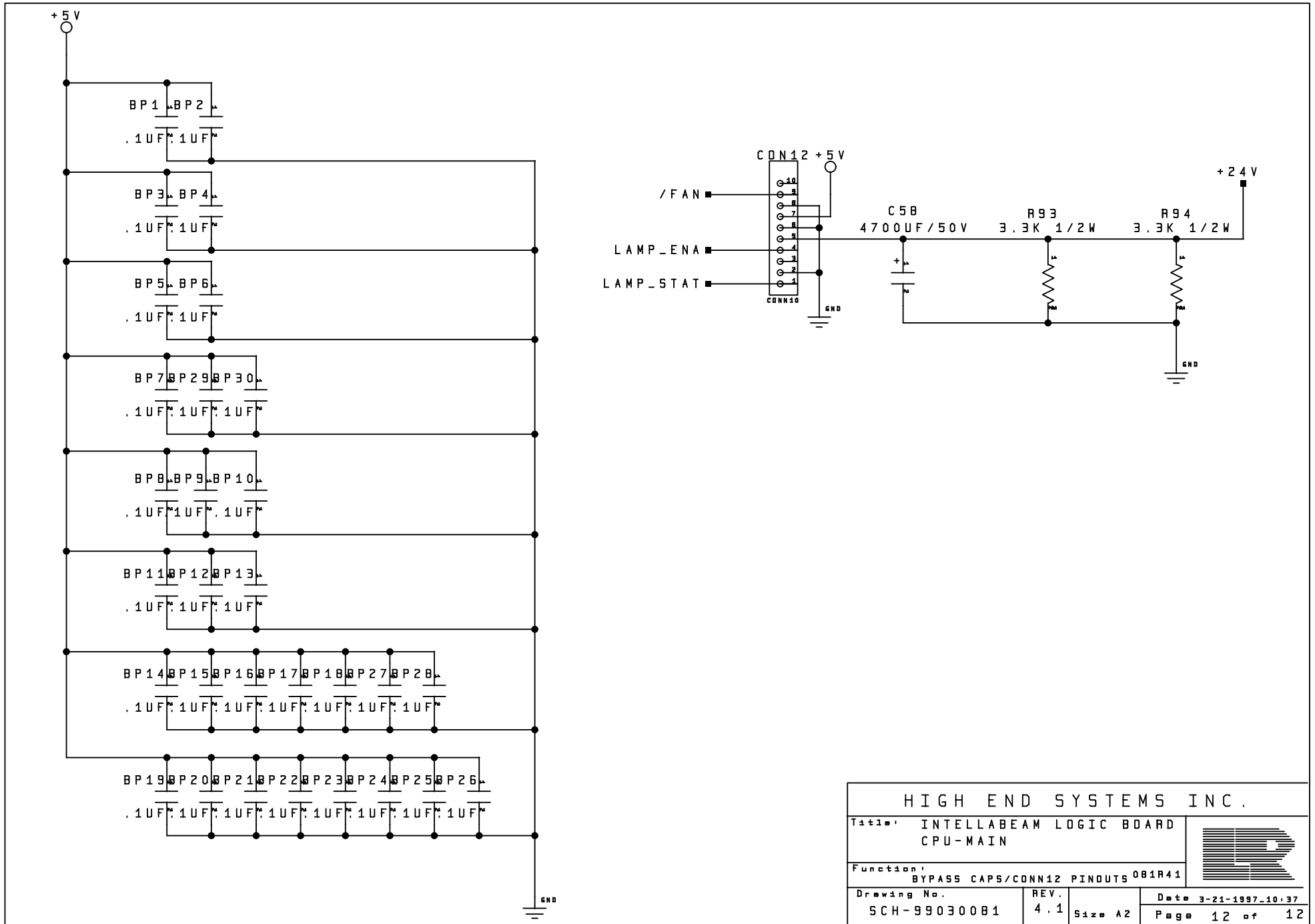
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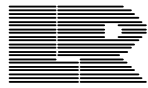
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RESISTOR VALUES ON CON 8&15,9&16,10&18,11&17 ARE TO BE DETERMINED



HIGH END SYSTEMS INC.			
Title: INTELLABEAM LOGIC BOARD			
CPU-MAIN			
Function: MOTOR DRIVERS		081R41	
Drawing No.	REV.	Size	Date
SCH-990300B1	4.1	A2	3-21-1997-10-37
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HIGH END SYSTEMS INC.			
Title: INTELLABEAM LOGIC BOARD CPU-MAIN			
Function: BYPASS CAPS/CONN12 PINOUTS 081R41			
Drawing No. SCH-990300B1	REV. 4.1	Size A2	Date 3-21-1997-10-37 Page 12 of 12